

# Fast Forecasting of Stock Market Prices by using New High Speed Time Delay Neural Networks

Hazem M. El-Bakry, Nikos Mastorakis

**Abstract**—Fast forecasting of stock market prices is very important for strategic planning. In this paper, a new approach for fast forecasting of stock market prices is presented. Such algorithm uses new high speed time delay neural networks (HSTDNNs). The operation of these networks relies on performing cross correlation in the frequency domain between the input data and the input weights of neural networks. It is proved mathematically and practically that the number of computation steps required for the presented HSTDNNs is less than that needed by traditional time delay neural networks (TTDNNs). Simulation results using MATLAB confirm the theoretical computations.

**Keywords**—Fast Forecasting, Stock Market Prices, Time Delay Neural Networks, Cross Correlation, Frequency Domain.

## I. INTRODUCTION

FORECASTING of stock market prices is very important for strategic planning in business companies. Because of strong competition between companies, such forecasting process is required to be done as fast as possible. With the advent of the digital computer, forecasting of stock market prices has since moved into the technological realm [36-68]. The most prominent technique involves the use of artificial neural networks (ANNs). Recently, time delay neural networks have shown very good results in different areas such as automatic control, speech recognition, blind equalization of time-varying channel and other communication applications. The main objective of this research is to reduce the response time of time delay neural networks. The purpose is to perform the testing process in the frequency domain instead of the time domain. Our approach was successfully applied for sub-image detection using high speed neural networks (HSNNs) as proposed in [5]. Furthermore, it was used for fast face detection [7,9,26], and fast iris detection [8]. Another idea to further increase the speed of HSNNs through image decomposition was suggested in [7].

HSNNs for detecting a certain code in one dimensional serial stream of sequential data were described in [10,27]. Compared with traditional neural networks, HSNNs based on cross correlation between the tested data and the input weights of neural networks in the frequency domain showed a

significant reduction in the number of computation steps required for certain data detection [5-35]. Here, we make use of the theory of HSNNs implemented in the frequency domain to increase the speed of time delay neural networks for market price prediction. The idea of moving the testing process from the time domain to the frequency domain is applied to time delay neural networks. Theoretical and practical results show that the proposed HSTDNNs are faster than TTDNNs.

The paper is organized as follows. Theory of neural networks for pattern detection is described in section II. Section III presents HSTDNNs for forecasting of stock market prices. Experimental results for fast forecasting of stock market prices by using HSTDNNs are given in section IV.

## II. THEORY OF ANNS FOR FORECASTING

Artificial neural network (ANN) is a mathematical model, which can be one or more layered that contain many artificial neural cells. The wide usage of the ANN may be due to the three basic properties:

1. The ability of the ANN as a parallel processing of the problems, for which if any of the neurons violate the constraints would not affect the overall output of the problem.
2. The ability of the ANN to extrapolate from historical data to generate forecasts.
3. The successful application of the ANN to solve non-linear problems. The history and theory of the ANN have been described in a large number of published literatures and will not be covered in this paper except for a very brief overview of how neural networks operate.

The ANN computation can be divided into two phases: learning phase and testing phase. The learning phase forms an iterative updating of the synoptic weights based upon the error back propagation algorithm. Back propagation algorithm is generalized of least mean square learning rule, which is an approximation of steepest descent technique. To find the best approximation, multi-layer feed forward neural network architecture with back propagation learning rule is used. A schematic diagram of typical multi-layer feed-forward traditional time delay neural network architecture is shown in Fig. 1. The number of neurons in the hidden layer is varied to give the network enough power to solve the forecasting problem. Each neuron computes a weighted sum of the individual inputs ( $I_1, I_2, \dots, I_j$ ) it receives and adding it with a bias ( $b$ ) to form the net input ( $x$ ). The bias is included in the neurons to allow the activation function to be offset from zero.

$$\text{sum} = w_{1,1} \cdot I_1 + w_{1,2} \cdot I_2 + \dots + w_{1,j} \cdot I_j + b \quad (1)$$

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Where,  $w_{ji}$  is the connection weight between neuron  $j$  and neuron  $i$ . The net input ( $sum$ ) is then passed to the subsequent layer through a non linear sigmoid function to form its own output ( $y_j$ ).

$$y_j = 1/(1 + e^{-sum}) \quad (2)$$

Afterward, the output  $y_j$  was compared with the target output  $t_j$  using an error function of the form:

$$\delta_k = (t_j - y_j) y_j (1 - y_j) \quad (3)$$

For the neuron in the hidden layer, the error term is given by the following equation:

$$\delta_j = y_j (1 - y_j) \sum_k \delta_k w_k \quad (4)$$

where  $\delta_k$  is the error term of the output layer, and  $w_k$  is the weight between the hidden layer and output layer. The error was then propagated backward from the output layer to the input layer to update the weight of each connection at iteration ( $t + 1$ ) as follows:

$$w_{ji}(t+1) = w_{ji}(t) + \eta \delta_j y_j + \alpha (w_{ji}(t) - w_{ji}(t-1)) \quad (5)$$

Choosing a small learning rate  $\eta$  leads to slow rate of convergence, and too large  $\eta$  leads to oscillation. The term  $\alpha$  is called momentum factor and determines the effect of past weight changes on the current direction of movement. Both of these constant terms are specified at the start of the training cycle and determine the speed and stability of the network. The process was repeated for each input pattern until the error was reduced to a threshold value.

### III. FORECASTING OF STOCK MARKET PRICES BY USING HSTDNNS

Computing the resulted output; for a certain pattern of information; in the incoming serial data, is a prediction problem. First neural networks are trained to predict the estimated variable and this is done in time domain. In pattern detection phase, each position in the incoming matrix is processed to predict the estimated variable by using neural networks. At each position in the input one dimensional matrix, each sub-matrix is multiplied by a window of weights, which has the same size as the sub-matrix. The outputs of neurons in the hidden layer are multiplied by the weights of the output layer. Thus, we may conclude that the whole problem is a cross correlation between the incoming serial data and the weights of neurons in the hidden layer.

The convolution theorem in mathematical analysis says that a convolution of  $f$  with  $h$  is identical to the result of the following steps: let  $F$  and  $H$  be the results of the Fourier Transformation of  $f$  and  $h$  in the frequency domain. Multiply  $F$  and  $H^*$  in the frequency domain point by point and then transform this product into the spatial domain via the inverse Fourier Transform. As a result, these cross correlations can be represented by a product in the frequency domain. Thus, by

using cross correlation in the frequency domain, speed up in an order of magnitude can be achieved during the detection process [5-35]. Assume that the size of the attack code is  $1 \times n$ . In attack detection phase, a sub matrix  $I$  of size  $1 \times n$  (sliding window) is extracted from the tested matrix, which has a size of  $1 \times N$ . Such sub matrix, which may be an attack code, is fed to the neural network. Let  $W_i$  be the matrix of weights between the input sub-matrix and the hidden layer. This vector has a size of  $1 \times n$  and can be represented as  $1 \times n$  matrix. The output of hidden neurons  $h(i)$  can be calculated as follows [5]:

$$h_i = g \left( \sum_{k=1}^n W_i(k) I(k) + b_i \right) \quad (6)$$

where  $g$  is the activation function and  $b(i)$  is the bias of each hidden neuron ( $i$ ). Equation 1 represents the output of each hidden neuron for a particular sub-matrix  $I$ . It can be obtained to the whole input matrix  $Z$  as follows [5]:

$$h_i(u) = g \left( \sum_{k=-n/2}^{n/2} W_i(k) Z(u+k) + b_i \right) \quad (7)$$

Eq.6 represents a cross correlation operation. Given any two functions  $f$  and  $d$ , their cross correlation can be obtained by [4]:

$$d(x) \otimes f(x) = \left( \sum_{n=-\infty}^{\infty} f(x+n) d(n) \right) \quad (8)$$

Therefore, Eq. 7 may be written as follows [5]:

$$h_i = g(W_i \otimes Z + b_i) \quad (9)$$

where  $h_i$  is the output of the hidden neuron ( $i$ ) and  $h_i(u)$  is the activity of the hidden unit ( $i$ ) when the sliding window is located at position ( $u$ ) and ( $u$ )  $\in [N-n+1]$ .

Now, the above cross correlation can be expressed in terms of one dimensional Fast Fourier Transform as follows [5]:

$$W_i \otimes Z = F^{-1} (F(Z) \bullet F^*(W_i)) \quad (10)$$

Hence, by evaluating this cross correlation, a speed up ratio can be obtained comparable to traditional neural networks. Also, the final output of the neural network can be evaluated as follows:

$$O(u) = g \left( \sum_{i=1}^q W_o(i) h_i(u) + b_o \right) \quad (11)$$

where  $q$  is the number of neurons in the hidden layer.  $O(u)$  is the output of the neural network when the sliding window located at the position ( $u$ ) in the input matrix  $Z$ .  $W_o$  is the weight matrix between hidden and output layer.

#### IV. COMPLEXITY ANALYSIS OF HSTDNNs FOR FORECASTING STOCK MARKET PRICES

The complexity of cross correlation in the frequency domain can be analyzed as follows:

1- For a tested matrix of  $1 \times N$  elements, the 1D-FFT requires a number equal to  $N \log_2 N$  of complex computation steps [13]. Also, the same number of complex computation steps is required for computing the 1D-FFT of the weight matrix at each neuron in the hidden layer.

2- At each neuron in the hidden layer, the inverse 1D-FFT is computed. Therefore,  $q$  backward and  $(1+q)$  forward transforms have to be computed. Therefore, for a given matrix under test, the total number of operations required to compute the 1D-FFT is  $(2q+1)N \log_2 N$ .

3- The number of computation steps required by HSTDNNs is complex and must be converted into a real version. It is known that, the one dimensional Fast Fourier Transform requires  $(N/2) \log_2 N$  complex multiplications and  $N \log_2 N$  complex additions [3]. Every complex multiplication is realized by six real floating point operations and every complex addition is implemented by two real floating point operations. Therefore, the total number of computation steps required to obtain the 1D-FFT of a  $1 \times N$  matrix is:

$$\rho = 6((N/2) \log_2 N) + 2(N \log_2 N) \quad (12)$$

which may be simplified to:

$$\rho = 5N \log_2 N \quad (13)$$

4- Both the input and the weight matrices should be dot multiplied in the frequency domain. Thus, a number of complex computation steps equal to  $qN$  should be considered. This means  $6qN$  real operations will be added to the number of computation steps required by HSTDNNs.

5- In order to perform cross correlation in the frequency domain, the weight matrix must be extended to have the same size as the input matrix. So, a number of zeros  $= (N-n)$  must be added to the weight matrix. This requires a total real number of computation steps  $= q(N-n)$  for all neurons. Moreover, after computing the FFT for the weight matrix, the conjugate of this matrix must be obtained. As a result, a real number of computation steps  $= qN$  should be added in order to obtain the conjugate of the weight matrix for all neurons. Also, a number of real computation steps equal to  $N$  is required to create butterflies complex numbers  $(e^{-jk(2\pi n/N)})$ , where  $0 < K < L$ . These  $(N/2)$  complex numbers are multiplied by the elements of the input matrix or by previous complex numbers during the computation of FFT. To create a complex number requires two real floating point operations. Thus, the total number of computation steps required for HSTDNNs becomes:

$$\sigma = (2q+1)(5N \log_2 N) + 6qN + q(N-n) + qN + N \quad (14)$$

which can be reformulated as:

$$\sigma = (2q+1)(5N \log_2 N) + q(8N-n) + N \quad (15)$$

6- Using sliding window of size  $1 \times n$  for the same matrix of  $1 \times N$  pixels,  $q(2n-1)(N-n+1)$  computation steps are required when using TTDNNs for certain attack detection or processing

( $n$ ) input data. The theoretical speed up factor  $\eta$  can be evaluated as follows:

$$\eta = \frac{q(2n-1)(N-n+1)}{(2q+1)(5N \log_2 N) + q(8N-n) + N} \quad (16)$$

HSTDNNs are shown in Fig. 2.

Time delay neural networks accept serial input data with fixed size ( $n$ ). Therefore, the number of input neurons equals to ( $n$ ). Instead of treating ( $n$ ) inputs, the proposed new approach is to collect all the incoming data together in a long vector (for example  $100 \times n$ ). Then the input data is tested by time delay neural networks as a single pattern with length  $L$  ( $L=100 \times n$ ). Such a test is performed in the frequency domain as described before.

The theoretical speed up ratio for searching short successive ( $n$ ) code in a long input vector ( $L$ ) using time delay neural networks is listed in tables I, II, and III. Also, the practical speed up ratio for manipulating matrices of different sizes ( $L$ ) and different sized weight matrices ( $n$ ) using a 2.7 GHz processor and MATLAB is shown in table IV.

An interesting point is that the memory capacity is reduced when using HSTDNNs. This is because the number of variables is reduced compared with TTDNNs.

#### V. CONCLUSION

A New technique for fast forecasting of stock market price has been presented. Such strategy has been realized by using our design for HSTDNNs. Theoretical computations have shown that HSTDNNs require fewer computation steps than traditional ones. This has been achieved by applying cross correlation in the frequency domain between the input data and the weights of neural networks. Simulation results have confirmed this proof by using MATLAB.

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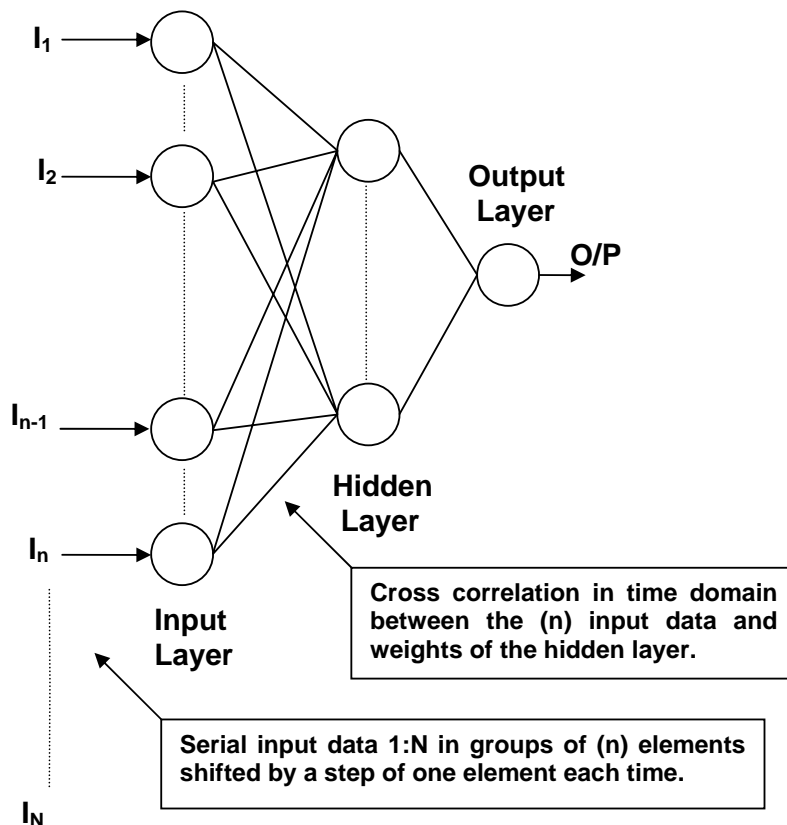


Fig. 1 TTDNNs for forecasting of stock market prices

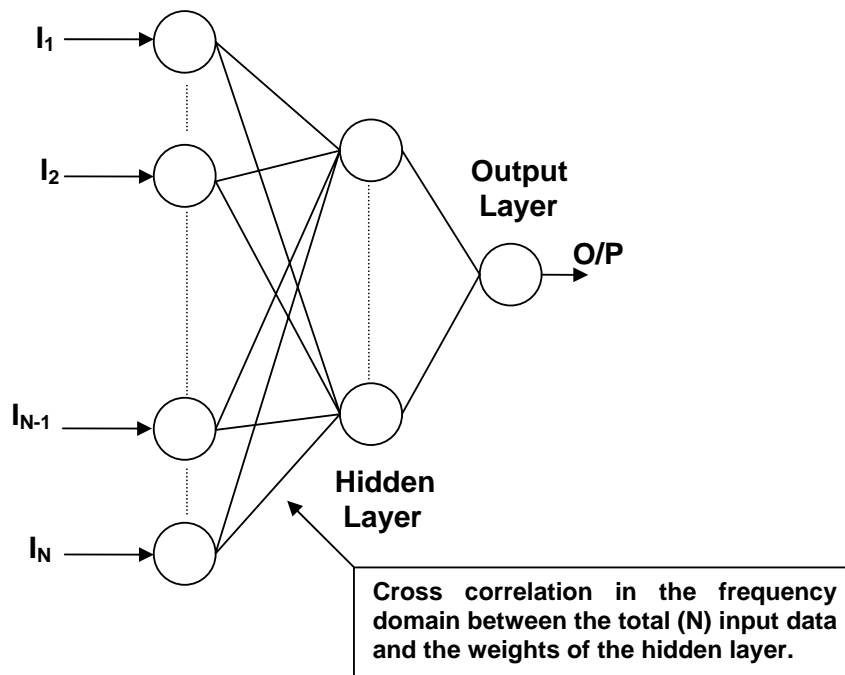


Fig. 2 HSTDNNs for forecasting of stock market prices

TABLE I  
THE THEORETICAL SPEED UP RATIO FOR FORECASTING OF STOCK MARKET PRICES (n=400)

Length of serial data	Number of computation steps required for TTDNNs	Number of computation steps required for HSTDNNs	Speed up ratio
10000	2.3014e+008	4.2926e+007	5.3613
40000	0.9493e+009	1.9614e+008	4.8397
90000	2.1478e+009	4.7344e+008	4.5365
160000	3.8257e+009	8.8219e+008	4.3366
250000	5.9830e+009	1.4275e+009	4.1912
360000	8.6195e+009	2.1134e+009	4.0786
490000	1.1735e+010	2.9430e+009	3.9876
640000	1.5331e+010	3.9192e+009	3.9119

TABLE II  
THE THEORETICAL SPEED UP RATIO FOR FORECASTING OF STOCK MARKET PRICES (n=625)

Length of serial data	Number of computation steps required for TTDNNs	Number of computation steps required for HSTDNNs	Speed up ratio
10000	3.5132e+008	4.2919e+007	8.1857
40000	1.4754e+009	1.9613e+008	7.5226
90000	3.3489e+009	4.7343e+008	7.0737
160000	0.5972e+010	8.8218e+008	6.7694
250000	0.9344e+010	1.4275e+009	6.5458
360000	1.3466e+010	2.1134e+009	6.3717
490000	1.8337e+010	2.9430e+009	6.2306
640000	2.3958e+010	3.9192e+009	6.1129

TABLE III  
THE THEORETICAL SPEED UP RATIO FOR FORECASTING OF STOCK MARKET PRICES (n=900)

Length of serial data	Number of computation steps required for TTDNNs	Number of computation steps required for HSTDNNs	Speed up ratio
10000	4.9115e+008	4.2911e+007	11.4467
40000	2.1103e+009	1.9612e+008	10.7600
90000	4.8088e+009	4.7343e+008	10.1575
160000	0.8587e+010	8.8217e+008	9.7336
250000	1.3444e+010	1.4275e+009	9.4178
360000	1.9381e+010	2.1134e+009	9.1705
490000	2.6397e+010	2.9430e+009	8.9693
640000	3.4493e+010	3.9192e+009	8.8009

TABLE IV  
PRACTICAL SPEED UP RATIO FOR FORECASTING OF STOCK MARKET PRICES

Length of serial data	Speed up ratio (n=400)	Speed up ratio (n=625)	Speed up ratio (n=900)
10000	8.94	12.97	17.61
40000	8.60	12.56	17.22
90000	8.33	12.28	16.80
160000	8.07	12.07	16.53
250000	7.95	17.92	16.30
360000	7.79	11.62	16.14
490000	7.64	11.44	16.00
640000	7.04	11.27	15.89