

# A Single Switch High Step-Up DC/DC Converter with Zero Current Switching Condition

Rahil Samani, Saeed Soleimani, Ehsan Adib, Majid Pahlevani

**Abstract**—This paper presents an inverting high step-up DC/DC converter. Basically, this high step-up DC/DC converter is an appealing interface for solar applications. The proposed topology takes advantage of using coupled inductors. Due to the leakage inductances of these coupled inductors, the power MOSFET has the zero current switching (ZCS) condition, which results in decreased switching losses. This will substantially improve the overall efficiency of the power converter. Furthermore, employing coupled inductors has led to a higher voltage gain. Theoretical analysis and experimental results of a 100W 20V/220V prototype are presented to verify the superior performance of the proposed DC/DC converter.

**Keywords**—Coupled inductors, high step-up DC/DC converter, zero-current switching, cuk converter, sepic converter.

## I. INTRODUCTION

THE nonstop pace of technology growth in recent decades has caused rapid growing demand for reliable energy sources all around the world. Global warming and other environmental consequences have drawn more attention to renewable energy resources like photovoltaic (PV) cells. PV array application has become popular in many fields like power generation for grid, standalone off-grid power stations, battery chargers, and etc. a general diagram of a two-stage power conditioning system for a grid connected PV system is illustrates in Fig. 1. Despite their clean power generation and long term working life, there are some aspects which need to be modified in practice, the regular output voltage of solar panels is about 20-30V that needs to be increased in order to inject power to the grid. Also, unstable characteristics of solar panels demand Maximum Power Point Tracking (MPPT) algorithm and proper DC/DC or DC/AC converter to achieve stable and regulated output power [1]-[5].

The traditional voltage-boosting non-isolated converters (e.g. boost converter and buck-boost converter) can theoretically show very high voltage conversion gain in high duty cycles but extreme large conduction losses affects both the efficiency and conversion ratio. Thus, their voltage gains would not be high enough, which is a serious limit. Furthermore, the high output voltage requires high voltage semiconductor devices. Consequently, a high-voltage switch would increase the switching loss and also high-voltage diodes reduce the efficiency due to serious reverse recovery

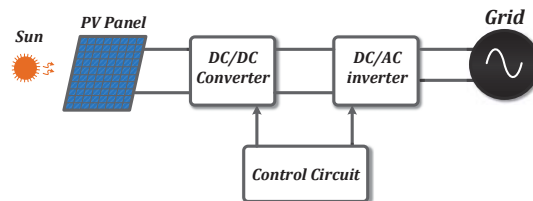


Fig. 1 Block Diagram of Grid-Connected PV Systems

issues. Also, cascade connection of aforementioned converters results in high gain but power is processed twice [6]-[10].

The main aim in designing a high step-up converter is to efficiently convert power while using minimum components to simplify the control method. So far, many different kinds of high step-up techniques have been applied such as coupled inductors and switched capacitors, [11]-[13]. Topologies based on coupled inductor suffer from stored energy in the leakage inductor which causes a voltage spike on main power MOSFET leading to higher power dissipation. Hence, active or passive clamp circuits are required. Transformer based topologies can give a very high output gain by adjusting the turn ratio. Serious draw-backs are increased number of controlled switches and large leakage inductance of transformer [14]-[21]. The converter presented in [22], considers a way to decrease the turn-off loss of the switch which enhances the efficiency of the converter and reducing current ripple while presenting a high boosting gain.

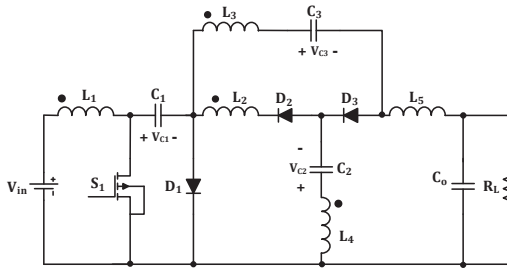
Some earlier high step-up circuits employ interleaved structures in order to reduce switch currents and increase the transferred power. However, two switches are required. The main objective of this work is to develop a high-efficiency high step-up topology as an interface for renewable energy resources. Such structures which combine coupled inductors and voltage-doublers are compatible with other topologies, especially in their voltage gain [23]-[25]. In [26], a single switch high step-up converter employing voltage-doubler on Sepic topology is presented. Although it has equal number of components to the proposed converter, the voltage gain of the proposed converter is noticeably higher which is due to the utilization of coupled inductors.

The main obtained advantages of the proposed converter are low operating duty cycles, low switch voltage stress, low size and cost of the circuit and high voltage conversion ratio. In the proposed converter, the switch voltage is inherently clamped and therefore, the energy of the leakage inductance of coupled inductors, which is usually problematic in high step-up converters, is absorbed and transferred to the output.

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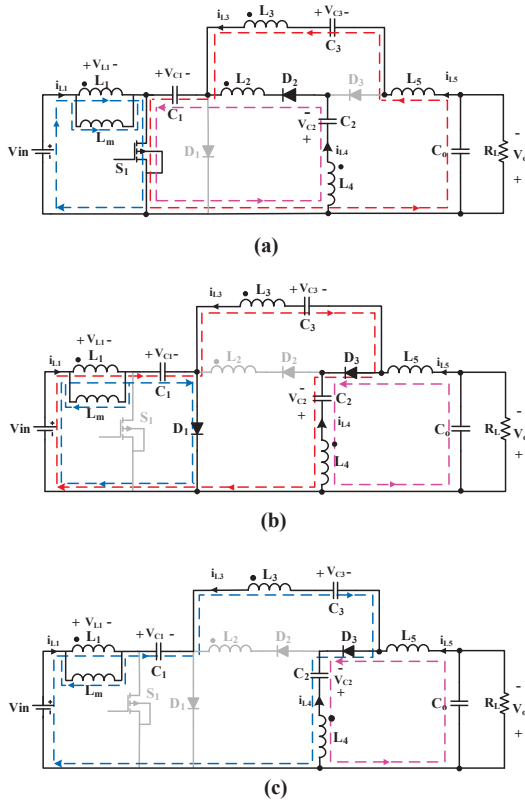


Fig. 4 Steady state waveforms of the proposed converter

$$V_{C2} - V_{C3} + 2nV_{C1} = 2nV_{in} \quad (7)$$

3) Mode III ( $t_2 \leq t < t_3$ ): Fig. 4 (c) shows the equivalent circuit during this time interval. As the voltage of the capacitor  $C_1$  ( $V_{C1}$ ) increases, the current through this capacitor decreases. When it reaches the current  $I_{C3}$ , the current through the diode  $D_1$  reaches zero. Therefore, the diode  $D_1$  stops conducting under ZCS condition. Since the diode  $D_1$  is off, in the next switching cycle the power MOSFET is turned on under ZCS. In this situation, the reverse recovery losses are eliminated.

$$V_{in} = V_{L1} + V_{C1} + V_{L3} + V_{C3} - V_{C2} + V_{L4} \quad (8)$$

By substituting (2), (3) and (7) in the above equation:

$$V_{L1} = V_{in} - V_{C1} \quad (9)$$

Equation (9) is equals to (5). This indicates that the voltage dropped on the inductor  $L_1$  in the third mode is equal to the voltage dropped on the inductor  $L_1$  in the second mode.

### III. OPERATING PRINCIPLE OF THE PROPOSED CONVERTER

In this section operating principles of the proposed converter is analyzed. In order to acquire the converter voltage gain, the volt-second-balance (VSB) equations of the input and the output inductors should be written.

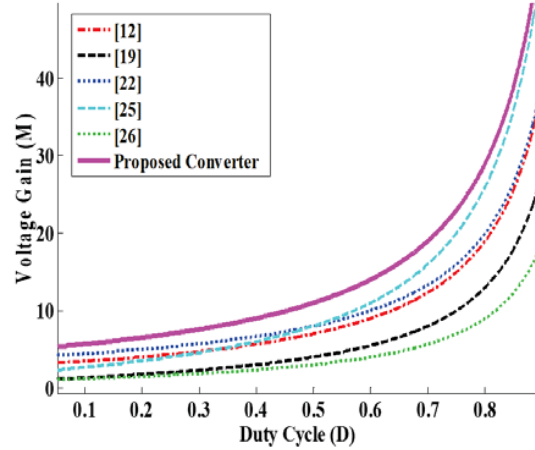


Fig. 5 Comparisons of the Voltage Gain versus Duty Cycle in the Proposed Converter and Conventional High Step-Up Converters

From (1), (5), and (9):

$$\int_{t_0}^{t_3} V_{L1} dt = DV_{in} + (1-D)(V_{in} - V_{C1}) = 0 \quad (10)$$

$$\int_{t_0}^{t_3} V_{L2} dt = D(V_o + nV_{in} - V_{C2} - V_{C3}) + \quad (11)$$

$$(1-D)(V_o + nV_{in} - V_{C2} - nV_{C1}) = 0$$

From (10), (11):

$$V_{C1} = \frac{V_{in}}{(1-D)T} \quad (12)$$

$$V_{C2} + DV_{C3} = V_o \quad (13)$$

Eventually, from (4), (7), and (13), the high step-up voltage gain is derived as:

$$M = \frac{V_o}{V_{in}} = \frac{2n+1+D}{1-D} \quad (14)$$

Considering the current through the magnetizing inductor  $L_m$  and the current-second-balance (CSB) equations in capacitors  $C_2$  and  $C_3$ , it can be achieved that:

$$I_{inave} = D(I_{Lm} + nI_{C2} + nI_{C3}) + (1-D)(I_{Lm} - nI'_{C2} - nI'_{C3}) \quad (15)$$

$$DT(I_{C2}) = (1-D)T(I'_{C2}) \quad (16)$$

$$DT(I_{C3}) = (1-D)T(I'_{C3}) \quad (17)$$

where  $I_{C3}$  and  $I_{C2}$  represent the charging currents through the capacitors  $C_3$  and  $C_2$ , respectively. Likewise,  $I'_{C3}$  and  $I'_{C2}$  represent the discharging currents through the capacitors. According to the equations in the operation modes:

$$I_{C3} = I_o \quad (18)$$

Since the converter is assumed ideal:

$$I_o = \frac{P_{in}}{V_{in}} \quad (19)$$

TABLE I  
SPECIFICATIONS OF THE PROPOSED CONVERTER

Parameter	Value
Input Inductor $L_1$	$100\mu H$
Coupled Inductors $L_2, L_3, L_4$	$400\mu H$
Output Inductor $L_5$	$3mH$
Diodes $D_2, D_3$	MUR860
Diode $D_1$	MUR1560
Capacitor $C_1$	$2.2\mu F$
Capacitor $C_2$	$1.5\mu F$
Capacitor $C_3$	$1\mu F$
Capacitor $C_o$	$4.7\mu F$
Power Switch $S_1$	IRF540
Turn Ratio ( $n = \frac{N_2}{N_1}$ )	2
Input Voltage $V_{in}$	20 V
Output Power $P_o$	100 W

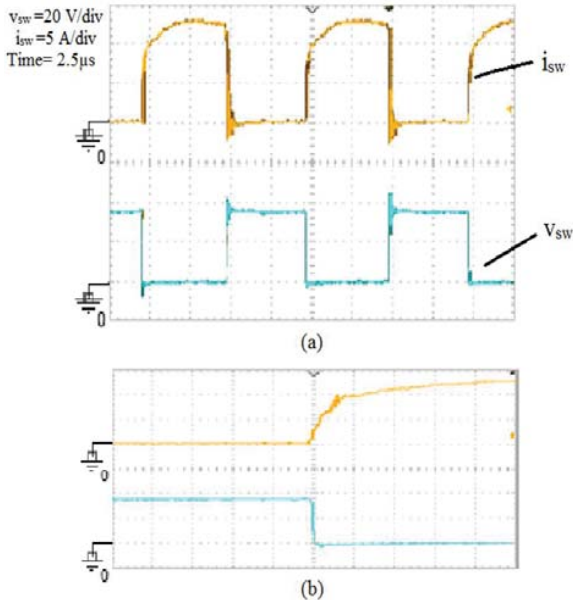


Fig. 6 (a) Voltage and Current Waveforms of the Power MOSFET  $S_1$  and (b) the Smooth Transition of the Power Switch Turn-On

And also from the CSB equation for the capacitor  $C_1$ :

$$D(nI_{C_2} + nI_{C_3}) = (1-D)(I_{L_m} - nI'_{C_2} - nI'_{C_3}) \quad (20)$$

The charging and discharging currents of these three capacitors are calculated from (14)-(20), which is used to determine the capacitances. Considering the basic power (20), it is obtained that:

$$P_{in} = (I_{in,ave}) \cdot V_{in} \quad (21)$$

From (15)-(18):

$$I_{L_m} = \frac{P_{in}}{V_{in}} \quad (22)$$

From (16)-(20):

$$I_{C_2} = \frac{(1-D)P_{in}}{2nDV_{in}} - \frac{P_{in}}{V_o} \quad (23)$$

The charging current through the capacitor  $C_1$  can be calculated based on (20) and (23). In Fig. 6, a comparison between the voltage gains of the proposed converter and other converters in [10], [17], [19], [20], [22], and [23] is



Fig. 7 The Output Voltage of the Prototype Circuit

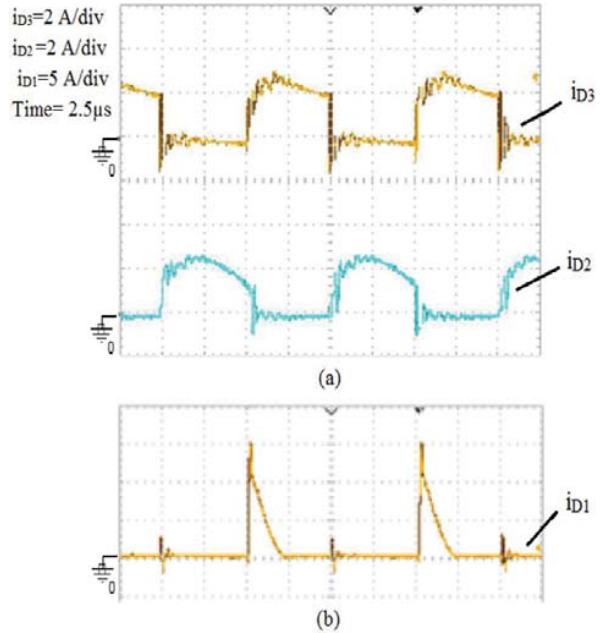


Fig. 8 Current Waveforms of Diodes (a)  $D_1, D_2$  and (b)  $D_3$

depicted. This comparison verifies the superior performance of the proposed converter.

#### IV. EXPERIMENTAL RESULTS

In order to verify the analysis of the proposed converter, a 100W prototype is implemented. The input voltage and switching frequency are 20V and 100 KHz, respectively. This circuit has been designed to have a 220V output voltage.

The turn ratio of the coupled inductors is 2. According to Eq. (14), the converter is designed to work with duty cycle of about 50%, which results in a voltage gain of 11. Specifications of the prototype circuit are listed in Table I. Fig. 6 (a) shows the voltage and current waveforms of the power MOSFET  $S_1$ . According to this figure, the power MOSFET  $S_1$  turns on with ZCS condition. In Fig. 6 (b), the smooth transition of the power MOSFET turn-on is shown. Fig. 7 shows the inverted output voltage of the proposed converter. The output voltage is about 220V, which verifies the theoretical analysis. Fig. 8 (a) depicts the currents  $i_{D_2}$  and  $i_{D_3}$  and Fig. 8 (b) shows the current  $i_{D_1}$ .



## V. CONCLUSION

This paper proposed a high step-up DC/DC converter with an inverted output polarity voltages. The improved gain makes this converter suitable for solar applications. Furthermore, merging this topology with a non-inverting converter will provide a bipolar structure for AC-module applications. The ZCS condition of the power MOSFET improves the overall efficiency of the proposed DC/DC converter. In addition, using a single switch with low on-state resistance has improved the cost, efficiency, and simplicity of the converter. There are no voltage spikes across the power MOSFET of this converter since the circuit clamps the voltage inherently and the leakage inductance energy of the coupled inductors is recovered. The experimental results of the prototype completely verify the theoretical analysis and the feasibility of the circuit.

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