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# ALD HfO<sub>2</sub> Based RRAM with Ti Capping

B. B. Weng, Z. Fang, Z. X. Chen, X. P. Wang, G. Q. Lo, and D. L. Kwong

Abstract—HfO<sub>x</sub> based Resistive Random Access Memory (RRAM) is one of the most widely studied material stack due to its promising performances as an emerging memory technology. In this work, we systematically investigated the effect of metal capping layer by preparing sample devices with varying thickness of Ti cap and comparing their operating parameters with the help of an Agilent-B1500A analyzer.

Keywords—HfO<sub>x</sub>, resistive switching, RRAM, metal capping.

### I. INTRODUCTION

RAM had received great attention over the past decade as various research groups reported promising capabilities such as fast programming speed, high scalability, good endurance and long retention [1], [2]. In addition, HfOx as the resistive switching layer was also acknowledged as one of the most favorable material [3], [4].

It is generally agreed that the mechanics behind the resistance switching (RS) as demonstrated by RRAM devices are related to reduction oxide reaction resulting in oxygen vacancy assisted conduction filaments formation and destruction [5], [6]. Therefore, we believe that the switching properties can be improved by increasing the density of oxygen vacancies and storing them in a so-called "oxygen reservoir" with the help of a metal electrode with high oxygen affinity. In our case, this is achieved with a Ti capping layer in between the RS layer and the top electrode.

# II. EXPERIMENT

After lasermark and RCA clean, a 500Å thick TiN layer was deposited on 8" silicon wafers as the bottom electrode (BE) by reactive sputtering. An 80Å HfO<sub>x</sub> RS layer is then deposited using Atomic Layer Deposition (ALD) on top of the BE. A varying thickness of Ti capping layer was covered on top of the RS layer followed by a 500Å TiN top electrode (TE) and then patterned with photo-lithography and dry etched to the BE. See Table I for the Ti capping split table. Electrical measurements are then performed using an Agilent-B1500A parametric analyzer.

B. B. Weng, Fang, Z. X. Chen, X. P. Wang, G. Q. Lo, and D. L. Kwong are with the Institute of Microelectronics, A\*STAR (Agency for Science, Technology and Research), 11 Science Park Road, Singapore 117685, (Tel: 65-67705628, Email: wengbb@ime.a-star.edu.sg ).

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TABLE I
TI CAPPING THICKNESS SPLIT

| TI CATTING THICKNESS STEET |      |         |          |      |
|----------------------------|------|---------|----------|------|
| WAFER                      | BE   | $HFO_X$ | CAP (TI) | TE   |
| S02                        | 500Å | 80Å     | 10Å      | 500Å |
| S05                        | 500Å | 80Å     | 50Å      | 500Å |
| S06                        | 500Å | 80Å     | 100Å     | 500Å |
| S07                        | 500Å | 80Å     | 200Å     | 500Å |
|                            |      |         |          |      |

#### III. ELECTRICAL CHARACTERIZATION

It is observed that the devices without a sufficient thickness of Ti capping layer does not demonstrate switching behavior; S01 (data not shown) and S02 which have no Ti cap and  $10\text{\AA}$  Ti cap respectively shows the forming process but failed in RS. S03 and S04 (data not shown) with  $20\text{\AA}$  and  $30\text{\AA}$  Ti cap respectively shows forming process as well as RS behavior, however, the on/off ratio of the device is very small <2 in all of the devices tested. It is worth mentioning that from S05 onwards with a Ti capping thickness of  $\geq$ 50Å, the device shows vast improvement in switching characteristics, surging from the <1 on/off ratio to >10 with a 50Å Ti thickness and >50 with a  $100\text{\AA}$  Ti thickness.

The result corresponds to previous reports made [7] and is most likely in agreement with our argument on an "oxygen reservoir".

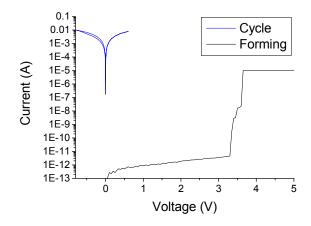


Fig. 1 S02 with 10Å Ti cap shows forming process in all the tested devices, but fail in resistance switching

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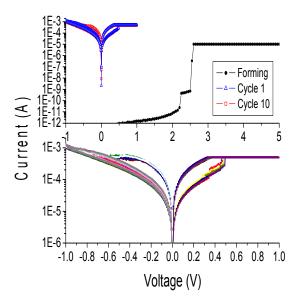


Fig. 2 S05 with 50Å Ti cap shows forming process and resistance switching but with small on/off ratio

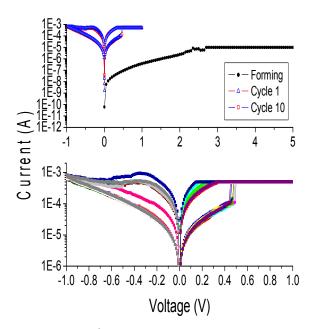


Fig. 3 S06 with 100Å Ti cap shows forming process and resistance switching with good on/off ratio

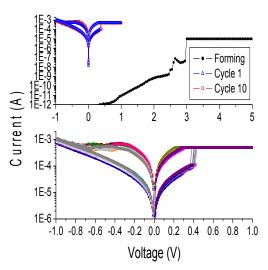


Fig. 4 S07 with 200Å Ti cap showing better uniformity and good on/off ratio as compared with S06

## IV. CONCLUSION

To conclude, we have studied the phenomena of resistance switching which is correlated to the redox reaction of oxygen vacancy related conductive filament formation and destruction. By investigating the effect of an "oxygen reservoir" with the help of a Ti capping with high oxygen affinity, we found that the performances of the same stack device such as uniformity, switching capability and on/off ratio can be improved dramatically with a thick enough capping layer, in our case >50Å.

# REFERENCES

- [1] I. G. Baek, D. C. Kim, M. J. Lee, H.-J. Kim, E. K. Yim, M. S. Lee, J. E. Lee, S. E. Ahn, S. Seo, J. H. Lee, J. C. Park, Y. K. Cha, S. O. Park, H. S. Kim, I. K. Yoo, U.-I. Chung, J. T. Moon, and B. I. Ryu, "Multi-layer cross-point binary oxide resistive memory (OxRRAM) for post-NAND storage application," in IEDM Tech. Dig., 2005, pp. 750–753.
- [2] H. Y. Lee, P. S. Chen, T. Y. Wu, Y. S. Chen, C. C. Wang, P. J. Tzeng, C. H. Lin, F. Chen, C. H. Lien, and M.-J. Tsai, "Low power and high speed bipolar switching with a thin reactive ti buffer layer in robust HfO2 based RRAM," in IEDM Tech. Dig., 2008, pp. 297–300.
- [3] Z. Fang, H. Y. Yu, X. Li, N. Singh, G. Q. Lo, and D. L. Kwong, "HfOx/TiOx/HfOx/TiOx Multilayer-Based Forming-Free RRAM Devices With Excellent Uniformity," Electron Device Letters, IEEE, vol. 32, pp. 566-568, 2011.
- [4] Z. Fang, H. Y. Yu, W. J. Liu, Z. R. Wang, X. A. Tran, B. Gao, and J. F. Kang, "Temperature instability of resistive switching on HfOx-based RRAM devices," IEEE Electron Device Letters, vol. 31, pp. 476-478, 2010.
- [5] D. C. Kim, S. Seo, S. E. Ahn, D.-S. Suh, M. J. Lee, B.-H. Park, I. K. Yoo, I. G. Baek, H.-J. Kim, E. K. Yim, J. E. Lee, S. O. Park, H. S. Kim, U.-I. Chung, J. T. Moon, and B. I. Ryu, "Electrical observations of filamentary conductions for the resistive memory switching in NiO films," Appl. Phys. Lett., vol. 88, no. 20, pp. 202 102-1–202 102-3, May 2006.
- [6] N. Xu, B. Gao, L. F. Liu, B. Sun, X. Y. Liu, R. Q. Han, J. F. Kang, and B. Yu, "A unified physical model of switching behavior in oxide based RRAM," in VLSI Symp. Tech. Dig., 2008, pp. 100–101.
- [7] H. Y. Lee, et al., "Low power and high speed bipolar switching with a thin reactive Ti buffer layer in robust HfO2 based RRAM," in 2008 IEEE International Electron Devices Meeting, pp. 1-4.