

# Space Vector PWM Simulation for Three Phase DC/AC Inverter

M. Kubeitari, A. Alhusayn, and M. Alnahar

**Abstract**—Space Vector Pulse Width Modulation SVPWM is one of the most used techniques to generate sinusoidal voltage and current due to its facility and efficiency with low harmonics distortion. This algorithm is specially used in power electronic applications. This paper describes simulation algorithm of SVPWM & SPWM using MatLab/simulink environment. It also implements a closed loop three phases DC-AC converter controlling its outputs voltages amplitude and frequency using MatLab. Also comparison between SVPWM & SPWM results is given.

**Keywords**— DC-AC Converter, MatLab, SPWM, SVPWM, Vref - rotating frame.

## I. INTRODUCTION

THE DC-AC converters are one of the most popular domains for research and development, because of its large utilization in industrial applications. As a result many algorithms in this domain was developed such as SVPWM, dq-rotating frame control, direct torque control DTC.....etc. These algorithms are able to control output frequency and amplitude of the power module of DC-AC converter.

## II. SPACE VECTOR MODULATION THEORY

The main idea behind SVPWM [1] , [2] is to divide the 2D-plane into six equal areas each of them is called sector. As shown in Fig.1. Each sector is determined by four vectors  $V_i, V_{i+1}$  where  $i \in \{1, \dots, 5\}$  these vectors are called active vectors because when these vectors are applied to the power module Fig. 2 the output voltage of the power module, will be greater than zero i.e. one of the switches  $S_a, S_b, S_c$  will not be off. The other two vectors  $V_0$  and  $V_7$  are called inactive vector, because all switches will be off or on. These two vectors allocate in the center of the circle C of Fig. 1.

The desired output assigned  $V_{ref}$  will rotate in angular velocity equal to desired output AC voltage frequency.

Rotating  $V_{ref}$  vector will scan all sectors with the time. For every sample time we can determine the sector containing  $V_{ref}$ , and calculate the time period for each vector of the determined sector.

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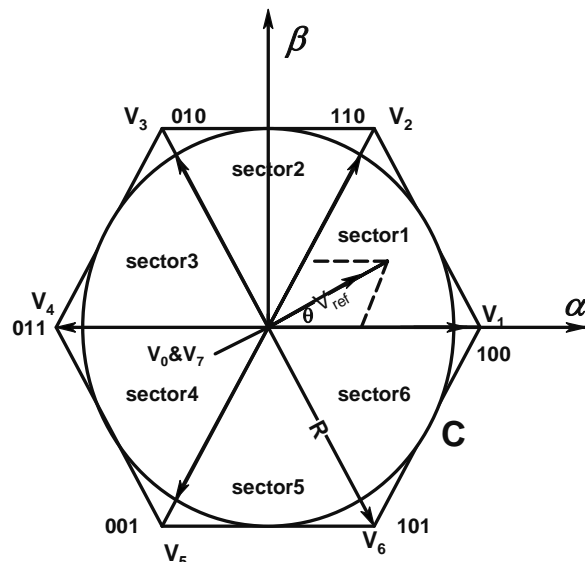


Fig. 1 SVPWM 2D plot

To each active vector corresponds a switch sequence Fig.2,  $S_a S_b S_c$  [3], for example the sequence:

$$S_a S_b S_c = 010 \quad (1)$$

$$S'_a S'_b S'_c = 101 \quad (2)$$

Corresponds to vector  $V_3$ .

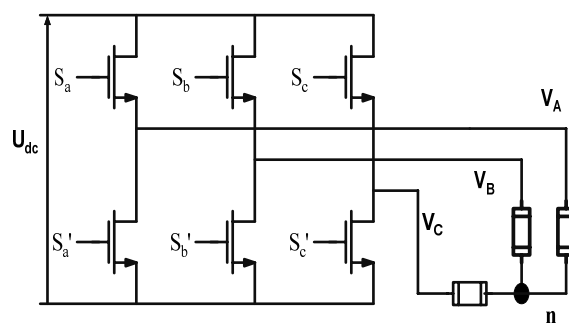


Fig. 2 Three legs power module

As we know that:

$$V_{An} = -\frac{U_{dc}}{3}, V_{Bn} = \frac{2U_{dc}}{3}, V_{Cn} = -\frac{U_{dc}}{3} \quad (3)$$

The three phase output voltage can be transformed from switch sequence to  $\alpha\beta 0$  using this matrix equation:

$$\begin{bmatrix} V_\alpha \\ V_\beta \end{bmatrix} = \frac{U_{dc}}{3} \begin{bmatrix} 2 & -1 & -1 \\ 0 & \sqrt{3} & -\sqrt{3} \end{bmatrix} \begin{bmatrix} S_a \\ S_b \\ S_c \end{bmatrix} \quad (4)$$

For  $V_3$ :

$$\begin{bmatrix} V_\alpha \\ V_\beta \end{bmatrix}_{(010)} = \frac{U_{dc}}{3} \begin{bmatrix} 2 & -1 & -1 \\ 0 & \sqrt{3} & -\sqrt{3} \end{bmatrix} \begin{bmatrix} 0 \\ 1 \\ 0 \end{bmatrix} \quad (5)$$

$$\begin{bmatrix} V_\alpha \\ V_\beta \end{bmatrix}_{(010)} = U_{dc} \begin{bmatrix} -\frac{1}{3} \\ \frac{1}{\sqrt{3}} \end{bmatrix} \quad (6)$$

From equation (6) we can write:

$$\theta = \tan^{-1} \left[ \frac{V_\beta}{V_\alpha} \right] = \frac{2\pi}{3} \quad (7)$$

$$V_{ref} = \sqrt{V_\alpha^2 + V_\beta^2} = \frac{2U_{dc}}{3} \quad (8)$$

Thus the vector corresponds to the sequence 010 is the vector  $V_3$ . The other switch sequence can be found similarly [4], as shown in Fig. 3.

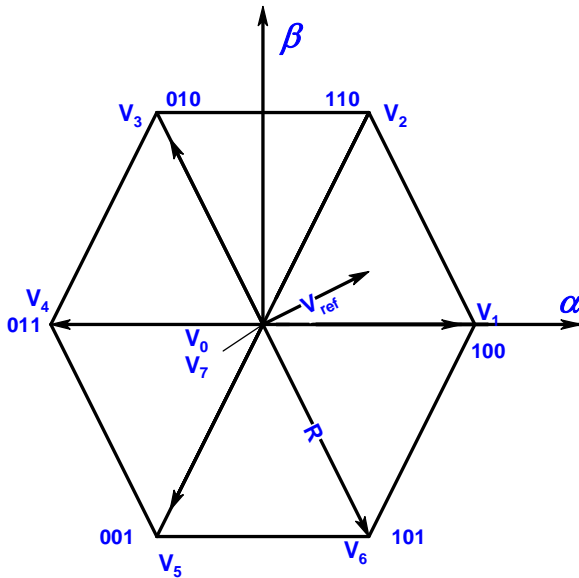


Fig. 3 SVPWM Vectors sequence

For each sector we can determine an ON/OFF time period to each switch. As an example let's take the sector number one see Fig. 4, this sector is defined by two active vectors  $V_1$  during the period  $t_a$  and  $V_2$  during the period  $t_b$  while inactive vectors  $V_0$  and  $V_7$  are applied during  $2t_0$ .

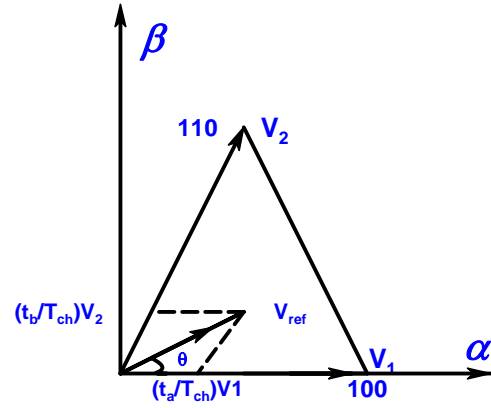


Fig. 4 Sector 1 Vectors

So the space vector  $V_{ref}$  is given by the following formula:

$$\int_0^{T_{ch}} V_{ref} dt = \int_0^{t_a} V_1 dt + \int_{t_a}^{t_a+t_b} V_2 dt + \int_{t_a+t_b}^{t_a+t_b+t_0} V_0 dt + \int_{t_a+t_b+t_0}^{T_{ch}} V_7 dt \quad (9)$$

In  $\alpha\beta 0$  coordination and from the above equation we can find:

$$t_a = T_{ch} \frac{3|V_{ref}| \sin(\frac{\pi}{3}-\theta)}{2U_{dc} \sin(\frac{\pi}{3})} \quad (10)$$

$$t_b = T_{ch} \frac{3|V_{ref}| \sin(\theta)}{2U_{dc} \sin(\frac{\pi}{3})} \quad (11)$$

$$t_0 = t_7 = \frac{T_{ch}-t_a-t_b}{2} \quad (12)$$

For sector number  $k$  we can generalize the above equations as follow:

$$t_a = T_{ch} \frac{3|V_{ref}| \sin(\frac{\pi}{3}k-\theta)}{2U_{dc} \sin(\frac{\pi}{3})} \quad (13)$$

$$t_b = T_{ch} \frac{3|V_{ref}| \sin(\theta)}{2U_{dc} \sin(\frac{\pi}{3}(k-1))} \quad (14)$$

$$t_0 = t_7 = \frac{T_{ch}-t_a-t_b}{2} \quad (15)$$

According to the sector which contains  $V_{ref}$  and the switch state we can determine the ON/OFF period time for each switch. As shown in Table I.

TABLE I  
SWITCH ON TIME FOR EACH PERIOD

Sector	STATE	Time Period of $T_{ch}$
1	$S_a=0$	$t_a+t_b+t_0$
	$S_b=0$	$t_b+t_0$
	$S_c=1$	$t_0$
2	$S_a=0$	$t_a+t_0$
	$S_b=1$	$t_a+t_b+t_0$
	$S_c=1$	$t_0$
3	$S_a=0$	$t_0$
	$S_b=1$	$t_a+t_b+t_0$
	$S_c=0$	$t_b+t_0$
4	$S_a=1$	$t_0$
	$S_b=1$	$t_a+t_0$
	$S_c=0$	$t_a+t_b+t_0$
5	$S_a=1$	$t_b+t_0$
	$S_b=0$	$t_0$
	$S_c=0$	$t_a+t_b+t_0$
6	$S_a=1$	$t_a+t_b+t_0$
	$S_b=0$	$t_0$
	$S_c=1$	$t_a+t_0$

### III. OVER MODULATION

Over-modulation occurs when the user tries to get output voltage with magnitude more than of that allocated inside the cotangent circle of hexagon. This is determined by active vector, as it depicted in Fig. 5:

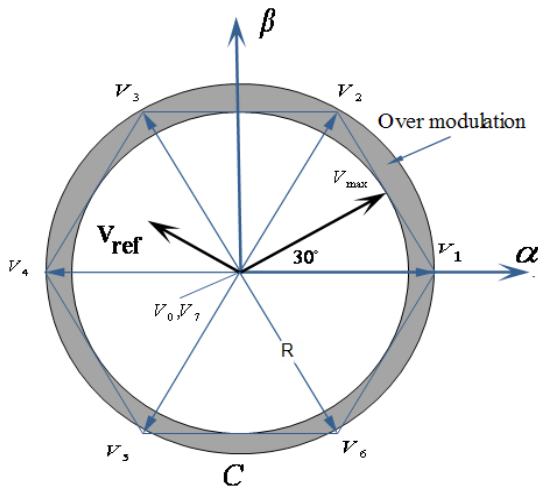


Fig. 5 Over Modulation region

### IV. SVPWM SIMULATION

The "SVPWM" simulation has been done using MatLab/Simulink environment [5], [6] which it is easy to implement such algorithm. Another reason for using MatLab is its ability to simulate power module such as DC-AC converter.

The block diagram of "SVPWM" simulation system, shown in Fig. 6, consists of five sections. The first section defines the inputs commands. These commands are the desired amplitude, frequency of the output voltage. Additional inputs

are the chopping time  $T_z$  and the input rail dc voltage. The second part is the modulation algorithm which is named "SVPWM". The third part which is "Embedded MatLab Function" contains a code (.m file). In this file we assign the threshold gate voltage to the upper and lower ON/OFF switches of the next block. Part four contains three Legs Bridge, LC filter and the load.

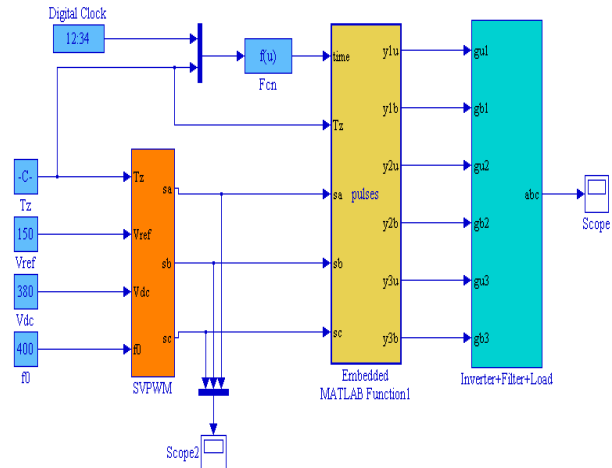


Fig. 6 Block diagram schematic of the overall system

The block diagram of SVPWM constructed using MatLab/Simulink is shown in Fig. 7:

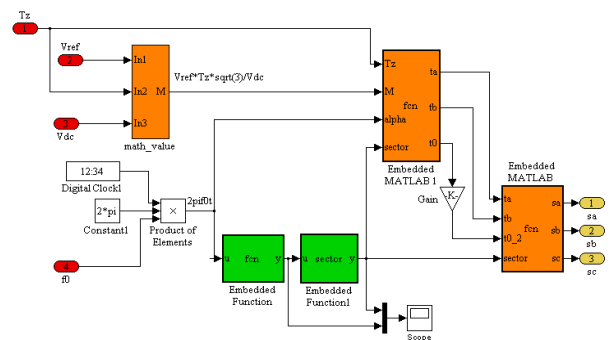


Fig. 7 Block diagram schematic of the SVPWM block

Part four of the overall simulated system is shown in Fig. 8. This part consists of three legs MOSFET transistor. MatLab/simulink environment permits us to enter the real MOSFET parameters [5], [6]. The simulation was done using IRFPC60 parameters. The cut off frequency of the LC filter shown in Fig. 9, Was  $f_c = 1600\text{Hz}$ . So  $L \approx 0.01\text{H}$ ,  $C = 1\mu\text{F}$ , while the load is three variable resistance.

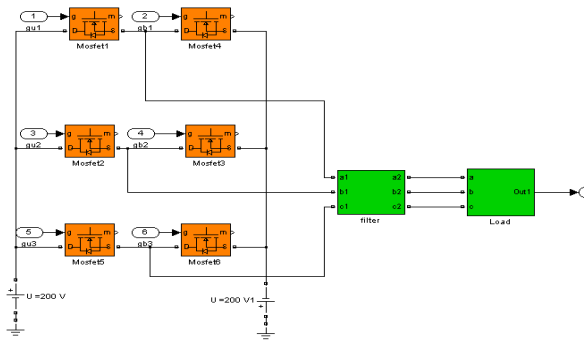


Fig. 8 Block diagram of the Power module, filter and load block

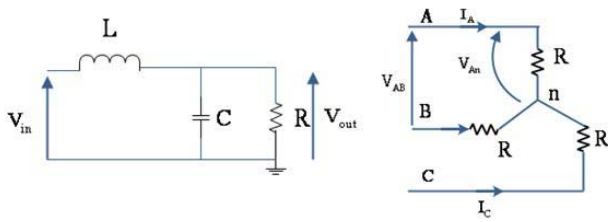


Fig. 9 LC Low Pass Filter and resistive load

#### V. SIMULATION RESULTS

After we have executed the overall simulink design of space vector PWM AC/DC converter using variable resistive load shown in Fig. 10, the simulation results of the output voltages ABC are shown in Fig. 11, which are three phases 120° out of phase pure sin wave.

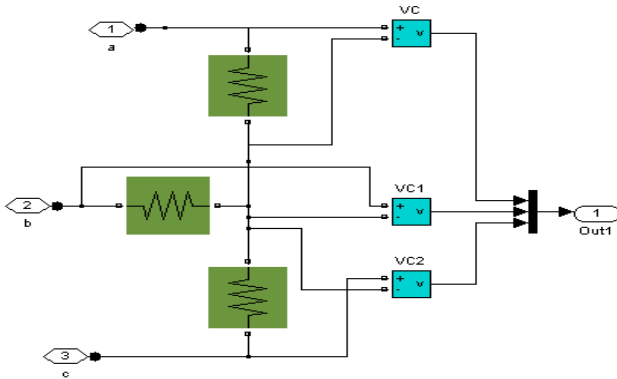


Fig. 10 Load Block diagram

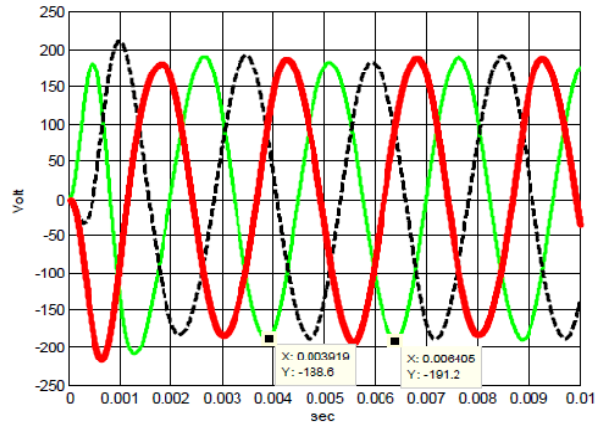
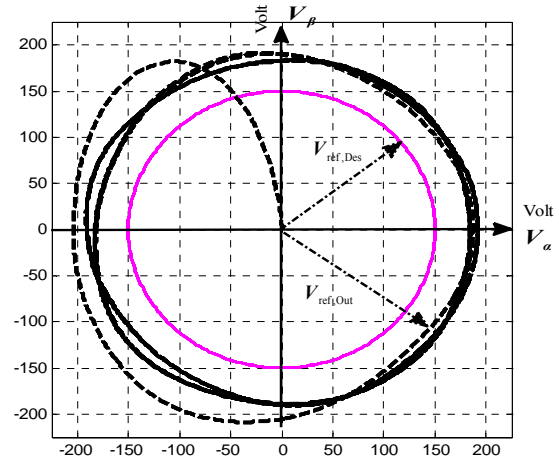


Fig. 11 Simulation Result

The SVPWM simulation results plot in  $\alpha\beta 0$  coordination of the output voltages ABC are shown in Fig. 11:

Fig. 12  $\alpha\beta 0$  simulation result of SVPWM

From Fig. 12, we notice that the overall system gain is greater than one.

#### VI. SPWM SIMULATION

The block diagram of the simulation system of Sine Pulse Width Modulation "SPWM" is indicated in Fig. 13. The SPWM contains two blocks, the first one, Fig. 14, is a SPWM generated by comparison between desired sine wave signal and triangle high frequency signal. The other block is the converter with a resistive load. The simulation results of the output voltages ABC are shown in Fig. 15:

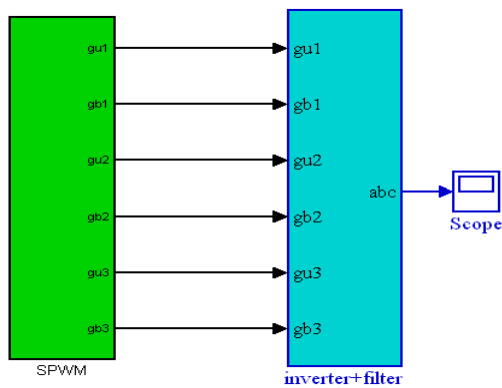


Fig.13 First block of SPWM

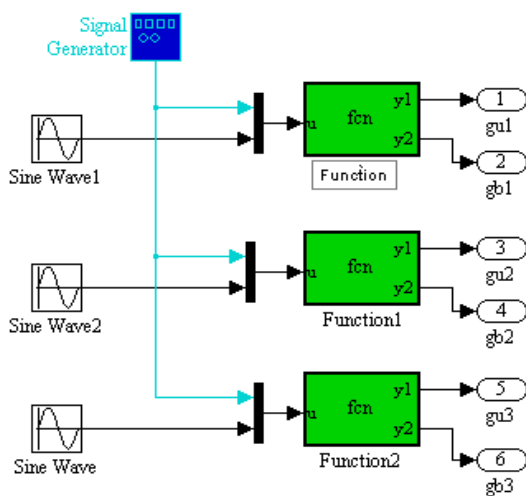


Fig. 14 Second block of SPWM

The output simulation results for SPWM as ABC phases are shown in Fig. 15:

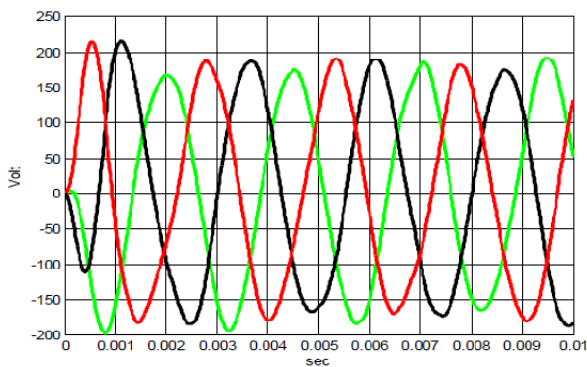
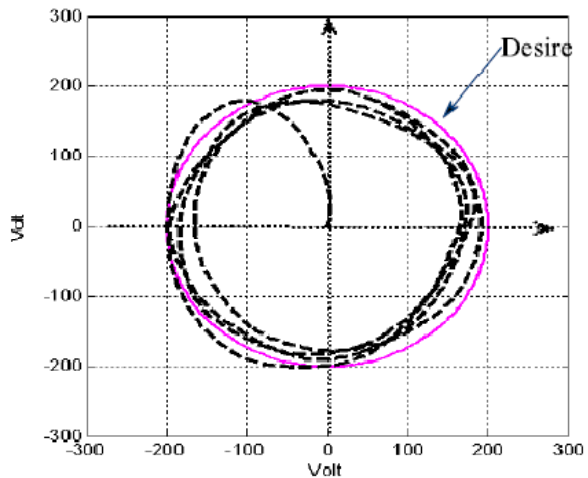


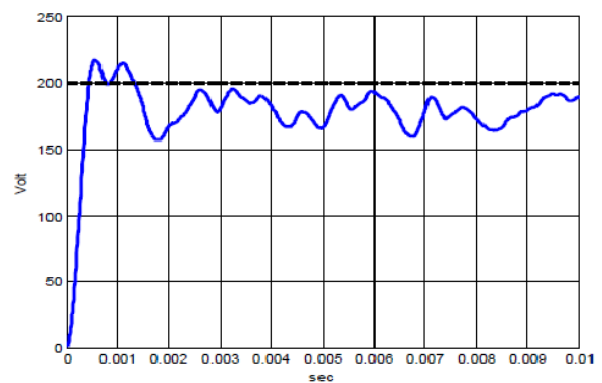
Fig. 15 ABC Simulation Result of SPWM

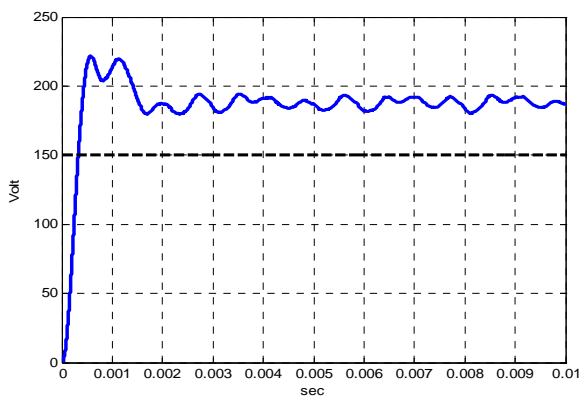
The SPWM simulation results plot in  $\alpha\beta 0$  coordination of the output voltages ABC are shown in Fig. 16:

Fig. 16  $\alpha\beta 0$  Simulation results of SPWM

## VII. COMPARISON BETWEEN SPWM AND SVPWM

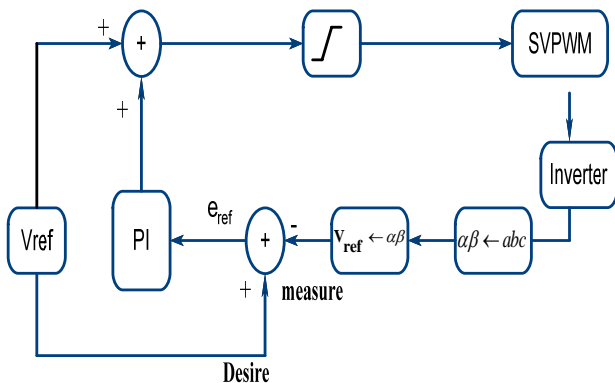
We plot the amplitude of vector  $V_{ref}$  versus time of the SVPWM and SPWM algorithms in Fig. 17 and Fig. 18. The comparison between these two plots shows that: for SPWM there are high fluctuations with respect to  $V_{ref}$  which can produce high harmonics distortion in the output voltages of the DC/AC converter. While in SVPWM the total harmonic distortion is smaller than SPWM. And the gain of overall system for SVPWM is greater than one.

Fig. 17  $V_{ref}$  plot of SPWM DC/AC Converter

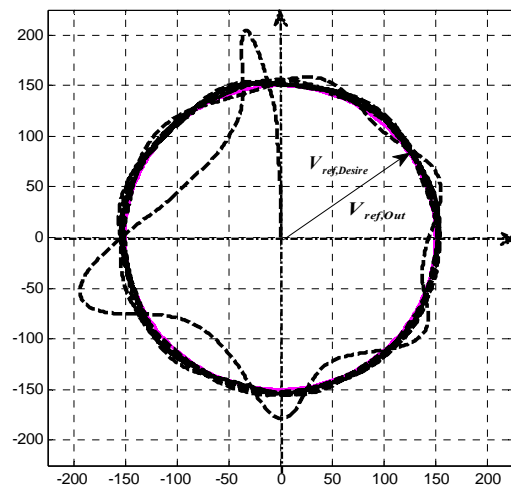
Fig. 18  $V_{ref}$  plot of SVPWM DC/AC Converter

#### VIII. CLOSED LOOP CONTROLLING OUTPUT VOLTAGE AMPLITUDE OF DC/AC CONVERTER

The control algorithm is based on dq rotating frame PI corrector. This control procedure is widely used to control the amplitude voltages of DC/AC converter [8]. In our simulation work we used a similar procedure, ( $V_{ref}$  Rotating frame control) the block diagram of closed loop control algorithm is shown in Fig. 19. It contains PI compensator, hard limiter,  $abc \rightarrow \alpha\beta 0 \rightarrow V_{ref}$  transformers and comparators. In this method the compensator pursues  $V_{ref}$  directly.

Fig. 19  $V_{ref}$  Rotating frame control block diagram

The Simulink result of this procedure is shown in Fig. 20, in  $\alpha\beta 0$ , which indicates a good tracking of output voltage with  $V_{ref}$ .

Fig. 20 Simulation Result of plot of  $V_{ref}$  Rotating frame control

#### IX. CONCLUSION

The simulation of DC/AC converter with SVPWM algorithm is verified using MatLab/Simulink. And the  $V_{ref}$  rotating frame algorithm is done in this algorithm. The simulation results have low harmonics distortion and good stability [7], as a function of the frequency and amplitude. After such simulation it is easy to implement [8], [9] this converter using hard-ware circuit such as FPGA or TMS, which is our next work.

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