

Power Flow and Modal Analysis of a Power System Including Unified Power Flow Controller

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Abstract—The Flexible AC Transmission System (FACTS) technology is a new advanced solution that increases the reliability and provides more flexibility, controllability, and stability of a power system. The Unified Power Flow Controller (UPFC), as the most versatile FACTS device for regulating power flow, is able to control respectively transmission line real power, reactive power, and node voltage. The main purpose of this paper is to analyze the effect of the UPFC on the load flow, the power losses, and the voltage stability using NEPLAN software modules, Newton-Raphson load flow is used for the power flow analysis and the modal analysis is used for the study of the voltage stability. The simulation was carried out on the IEEE 14-bus test system.

Keywords—FACTS, load flow, modal analysis, UPFC, voltage stability.

I. INTRODUCTION

THE increasing electric power demand and the restrictions on the power system expansion due to the high cost and environmental issues are the main factors forcing the power systems to operate under increasingly stressed conditions, the ability to maintain voltage stability becomes a growing concern. [1]

The voltage instability of power systems is taking part in increasing the losses in power systems, which is not acceptable, and the events accompanying voltage instability may have disastrous effects, including a resultant low voltage profile in a significant area of the power network, known as the voltage collapse phenomenon [2], [3].

A voltage collapse can be initiated by either a primary fault or an unexpected load demand increase, in combination with insufficient reactive power reserves or transmission capability. [3]

Voltage collapse occurs when increased loading leads to a loss of voltage control in a significant part of a power system. Though voltage instability is a local phenomenon, it may gradually develop a global problem without quick responses. One of the best solutions to avoid voltage instability and to enhance controllability and increase power transfer capability

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and reduce losses is the Flexible AC Transmission Systems (FACTS) [4]. FACTS is a static equipment used for controlling the power transmission system [5], [6]. FACTS is defined as "a power electronic based system and other static equipment that provide control of one or more AC transmission system parameters to enhance controllability and increase power transfer capability" [7], [8].

Unified power flow controller (UPFC), being the most versatile FACTS device, can control, concurrently or selectively, the transmission-line impedance, the magnitude of terminal voltage, and the active and reactive power flow in the line and thus, extensively considered for the transmission line compensation [9], [10].

II. UNIFIED POWER FLOW CONTROLLER

The UPFC is one of the FACTS devices, which provides the independent control of the real and reactive power flow, voltage magnitude and enhance the dynamic stability of the system. The UPFC consists of two switching converters like series converter and shunt converter operated from a common DC link. The converters are connected to the power system via coupling transformers. The UPFC structure can be described in Fig. 1 [8].

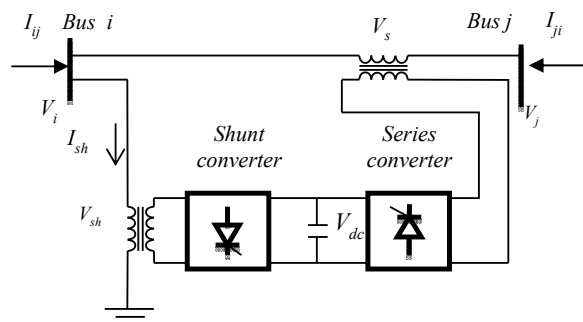


Fig. 1 UPFC Configuration

The series converter provides the main function of the UPFC by injecting a voltage V_{se} with controllable magnitude and phase angle in series with the line via an insertion transformer. The transmission line current flows through this voltage source resulting in reactive and real power exchange between it and the AC system. The reactive power exchanged at the AC terminal (i.e. at the terminal of the series insertion transformer) is generated internally by the converter. The real power exchanged at the AC terminal is converted into DC power, which appears at the DC link as a positive or negative real power demand.

The basic function of the shunt converter is to supply or absorb the real power demanded by the series converter at the common DC link to support the real power exchange resulting from the series voltage injection. This DC link power demand of the series converter is converted back to AC by the shunt converter and coupled to the transmission line bus via a shunt-connected transformer. In addition, the shunt converter can also generate or absorb controllable reactive power, if it is desired, and thereby provide independent shunt reactive compensation for the line. It is important to note that whereas there is a closed direct path for the real power resulting from the series voltage injection between the UPFC and the transmission line, the corresponding reactive power exchanged is generated or absorbed locally by the series converter and therefore does not have to be transmitted by the line. Thus, the shunt converter can be operated at a unity power factor or it can be controlled to have a reactive power exchange with the line independent of the reactive power exchanged by the series converter. Obviously, there can be no reactive power flow through the UPFC DC link [11], [12].

III. UPFC LOAD FLOW MODEL

Reference [13] has presented an approach of modeling UPFC device in load flow studies. This approach is based on injected active and reactive power at the terminals of UPFC. This UPFC model will be referred as UPFC injection model [14].

A. Series Connected Voltage Source Converter Model

Suppose a series connected voltage source is located between nodes i and j in a power system. The series voltage source converter can be modeled with an ideal series voltage V_s in series with a reactance X_s . In Fig. 2, V_s models an ideal voltage source and represents a fictitious voltage behind the series reactance [15]:

$$\vec{V}_i' = \vec{V}_s + \vec{V}_i \quad (1)$$

The series voltage source is controllable in magnitude and phase, i.e.:

$$\vec{V}_s = r\vec{V}_i e^{j\gamma} \quad (2)$$

where $0 \leq r \leq r_{max}$ and $-\pi \leq \gamma \leq \pi$

The equivalent circuit vector diagram is shown in Fig. 3.

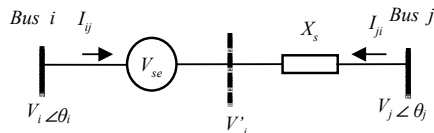


Fig. 2 Representation of a series connected VSC

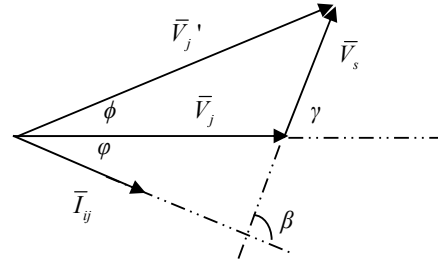


Fig. 3 Vector diagram of the equivalent circuit of VSC

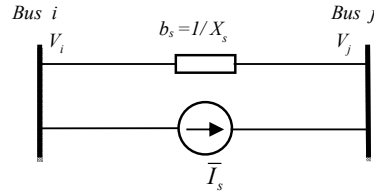


Fig. 4 Replacement of a series voltage source by a current source

The current sources I_s corresponds to the injection powers S_{is} and S_{js} , where:

$$\begin{aligned} S_{is} &= V_i (-I_{se})^* \\ S_{js} &= V_j (I_{se}) \end{aligned} \quad (3)$$

The injection powers S_{is} and S_{js} are simplified to:

$$S_{is} = \vec{V}_i (jb_s r \vec{V}_i e^{j\gamma})^* = -b_s r V_i^2 \sin \gamma - jb_s r V_i^2 \cos \gamma \quad (4)$$

If we define: $\theta_j = \theta_i - \theta_j$ we have,

$$S_{js} = \vec{V}_j (-jb_s r \vec{V}_i e^{j\gamma})^* = b_s r V_i V_j \sin(\theta_j + \gamma) + jb_s r V_i V_j \cos(\theta_j + \gamma) \quad (5)$$

B. UPFC Model

In UPFC, the shunt connected voltage source (Converter 1) is used mainly to provide the active power, which is injected, to the network via the series connected voltage source:

$$P_{CONV1} = P_{CONV2}$$

The equality above is valid when the losses are neglected. The apparent power supplied by the series voltage source converter is calculated from:

$$S_{CONV2} = \vec{V}_{se} \vec{I}_{ij}^* = r e^{j\gamma} \vec{V}_i \left(\frac{\vec{V}_i - \vec{V}_j}{jX_s} \right)^* \quad (6)$$

Active and reactive powers supplied by Converter 2 are distinguished as:

$$P_{CONV2} = r b_s V_i V_j \sin(\theta_i - \theta_j + \gamma) - r b_s V_i^2 \sin \gamma \quad (7)$$

$$Q_{series} = -rb_s V_i V_j \cos(\theta_i - \theta_j + \gamma) + rb_s V_i^2 \cos \gamma + r^2 b_{se} V_i^2 \quad (8)$$

The reactive power delivered or absorbed by converter 1 is independently controllable by UPFC and can be modeled as a separate controllable shunt reactive source. we assume that $Q_{CONVI} = 0$. Consequently, the UPFC injection model is constructed from the series connected voltage source model (Fig. 5) with the addition of a power equivalent to $P_{CONV} + j0$ to node i . Thus, the UPFC injection model is shown in Fig. 6. The model shows that the net active power interchange of UPFC with the power system is zero, as is it expected for a lossless UPFC.

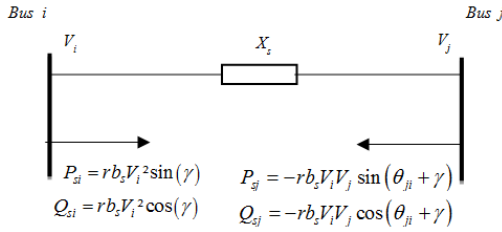


Fig. 5 Injection model for a series connected VSC

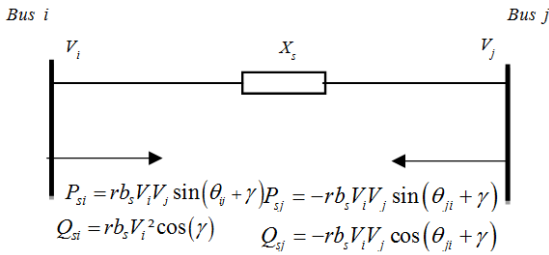


Fig. 6 UPFC model

C. Series Connected Voltage Source Converter Model

The UPFC injection model can easily be incorporated in a load flow program. If a UPFC is located between node i and node j in a power system, the admittance matrix is modified by adding a reactance equivalent to X_s between node i and node j . The Jacobian matrix is modified by addition of appropriate injection powers. If we consider the linearized load flow model as:

$$\begin{bmatrix} \Delta P \\ \Delta Q \end{bmatrix} = \begin{bmatrix} H & N \\ J & L \end{bmatrix} \begin{bmatrix} \Delta \Theta \\ \Delta V \end{bmatrix} \quad (9)$$

The Jacobian matrix is modified as given in Table I (The superscript 0 denotes the Jacobian elements without UPFC) [15].

IV. MODAL ANALYSIS

The Modal analysis mainly depends on the power-flow Jacobian matrix of (9). Gao, Morison, and Kundur [16] proposed this method in 1992. It can predict voltage collapse in complex power system networks. It involves mainly the

computing of the smallest eigenvalues and associated eigenvectors of the reduced Jacobian matrix obtained from the load flow solution. The eigenvalues are associated with a mode of voltage and reactive power variation which can provide a relative measure of proximity to voltage instability. Then, the participation factor can be used effectively to find out the weakest nodes or buses in the system. The analysis is expressed as follows [17]:

Equation (9) can be rewritten as:

$$\begin{bmatrix} \Delta P \\ \Delta Q \end{bmatrix} = \begin{bmatrix} J_{11} & J_{12} \\ J_{21} & J_{22} \end{bmatrix} \begin{bmatrix} \Delta \Theta \\ \Delta V \end{bmatrix} \quad (10)$$

By letting $\Delta P=0$ in (10)

$$\Delta P = 0 = J_{11} \Delta \Theta + J_{12} \Delta V, \quad \Delta \Theta = -J_{11}^{-1} J_{12} \Delta V \quad (11)$$

$$\Delta Q = J_{21} \Delta \Theta + J_{22} \Delta V \quad (12)$$

Substituting (11) in (12):

$$\Delta Q = J_R \Delta V \quad (13)$$

where:

$$J_R = [J_{22} - J_{21} J_{11}^{-1} J_{12}] \quad (14)$$

J_R is the reduced Jacobian matrix of the system.

Equation (13) can be written as

$$\Delta V = J_R^{-1} \Delta Q \quad (15)$$

TABLE I MODIFICATION OF THE JACOBIAN MATRIX	
$H_{(i,i)} = H_{(i,i)}^0 - Q_{sj}$	$N_{(i,i)} = N_{(i,i)}^0 - P_{sj}$
$H_{(i,j)} = H_{(i,j)}^0 + Q_{sj}$	$N_{(i,j)} = N_{(i,j)}^0 - P_{sj}$
$H_{(j,i)} = H_{(j,i)}^0 + Q_{sj}$	$N_{(j,i)} = N_{(j,i)}^0 + P_{sj}$
$H_{(j,j)} = H_{(j,j)}^0 - Q_{sj}$	$N_{(j,j)} = N_{(j,j)}^0 + P_{sj}$
$J_{(i,i)} = J_{(i,i)}^0$	$L_{(i,i)} = L_{(i,i)}^0 + 2Q_{sj}$
$J_{(i,j)} = J_{(i,j)}^0$	$L_{(i,j)} = L_{(i,j)}^0$
$J_{(j,i)} = J_{(j,i)}^0 - P_{sj}$	$L_{(j,i)} = L_{(j,i)}^0 + Q_{sj}$
$J_{(j,j)} = J_{(j,j)}^0 + P_{sj}$	$L_{(j,j)} = L_{(j,j)}^0 + Q_{sj}$

The matrix J_R represents the linearized relationship between the incremental changes in bus voltage (ΔV) and bus reactive power injection (ΔQ). It's well known that, the system voltage is affected by both real and reactive power variations. In order to focus the study of the reactive demand and supply problem of the system as well as minimize computational effort by reducing dimensions of the Jacobian matrix J the real power ($\Delta P=0$) and angle part from the system in (10) are eliminated.

The eigenvalues and eigenvectors of the reduced order Jacobian matrix J_R are used for the voltage stability characteristics analysis. Voltage instability can be detected by identifying modes of the eigenvalues matrix J_R . The magnitude of the eigenvalues provides a relative measure of proximity to instability. The eigenvectors on the other hand present information related to the mechanism of loss of voltage stability.

Modal analysis of J_R results in:

$$J_R = \lambda \Phi \xi \quad (16)$$

where Φ = right eigenvector matrix of J_R , ξ = left eigenvector matrix of J_R , λ = diagonal eigenvalue matrix of J_R

Equation (16) can be written as:

$$J_R^{-1} = \Phi \lambda^{-1} \xi \quad (17)$$

In general, it can be said that, a system is voltage stable if the eigenvalues of J_R are all positive. This is different from dynamic systems where eigenvalues with negative real parts are stable. The relationship between system voltage stability and eigenvalues of the J_R matrix is best understood by relating the eigenvalues with the V-Q sensitivities of each bus (which must be positive for stability). J_R can be taken as a symmetric matrix and therefore the eigenvalues of J_R are close to being purely real. If all the eigenvalues are positive, J_R is positive definite and the V-Q sensitivities are also positive, indicating that the system is voltage stable.

The system voltage is considered unstable if at least one of the eigenvalues is negative. A zero eigenvalue of J_R means that the system is on the verge of voltage instability. Furthermore, small eigenvalue of J_R determines the proximity of the system to being voltage unstable [18].

There is no need to evaluate all the eigenvalues of J_R of a large power system because it is known that once the minimum eigenvalues become zero, the system Jacobian matrix becomes singular and voltage instability occurs. Therefore, the eigenvalues of importance are the critical eigenvalues of the reduced Jacobian matrix J_R . Thus, the smallest eigenvalues of J_R are taken to be the least stable modes of the system. The rest of the eigenvalues are neglected because they are considered to be strong enough modes. Once the minimum eigenvalues and the corresponding left and right eigenvectors have been calculated the participation factor can be used to identify the weakest node or bus in the system.

The appropriate definition and determination as to which node or load bus participates in the selected modes become very important. This necessitates a tool, called the participation factor, for identifying the weakest nodes or load buses that are making significant contribution to the selected modes [19]:

$$\Delta V = \sum_i \frac{\Phi_i \xi_i}{\lambda_i} \Delta Q \quad (18)$$

where λ_i is the i^{th} eigenvalue, Φ_i is the i^{th} column right eigenvector and ξ_i is the i^{th} row left eigenvector of matrix J_R . Each eigenvalue λ_i and corresponding right and left eigenvectors Φ_i and ξ_i , defined the i mode of the system.

A. Identification of the Weak Load Buses

The minimum eigenvalues, which become close to instability, need to be observed more closely. The relationship between system voltage stability and eigenvalues of the J_R

matrix is best understood by relating the eigenvalues with the V-Q sensitivities of each bus (which must be positive for stability). J_R can be taken as a symmetric matrix and therefore the eigenvalues of J_R are close to being purely real. If all the eigenvalues are positive, J_R is positive definite and the V-Q sensitivities.

B. V-P Curves

The V-P curve for a bus is obtained by increasing the total load at the bus or area and performing successive power flow calculations, whilst maintaining a constant power factor. The voltage at the bus is plotted as a function of the total active power load, using conventional power-flow programs until the bifurcation point or 'nose' point is reached at critical loading.

The points above the 'nose' correspond to voltage stable operation. Continuation power-flow programs can be used to obtain solutions below the bifurcation point, but this is not usually necessary [20]. Generating V-P curves via automation of power flow simulations can be implemented relatively easily. As the size of the power network increases however, the processing time required to generate V-P curve increases, due to the increase in processing time to perform a single power flow solution. For large networks, processing time can be relatively large. More importantly, V-P curves at several buses must be generated before a system-wide perspective of voltage stability emerges, since there is no way of identifying beforehand the buses that are critical. For large power networks this can be time-consuming and confusing. V-P curves also give no useful insight into the cause of the voltage stability problem in the network [21].

C. Q-V Curves

Many power system planners still utilize Q-V curves in analyzing performance criteria [5]. Such curves indicate the sensitivity and variation of bus voltages with respect to reactive power injections and absorptions. The Q-V curve at a test bus is generated by placing a variable reactive power source with infinite limits at the bus. Successive power flows are performed for different scheduled values of bus voltage, and the required reactive power injection is measured. Q-V curves can therefore give the reactive power load margin at a bus from a stable operating point to the point of voltage instability. Such curves give no insight however into the causes of the voltage stability, such as participating nodes and branches. As with V-P curves, Q-V curves at several buses need to be drawn to obtain a system-wide perspective of the stability problem and decipher which buses in the system have the smallest stability margins, since there is no way of identifying beforehand which buses are critical. Although computation can be easily implemented on software, processing time can be quite large for practical networks with several hundred buses [22].

V. SYSTEM UNDER STUDY

Simulation studies were done for IEEE 14-bus test system, data of the 14-bus system contains 20 lines, 5 generators are taken from [12], and the test system is shown in Fig. 7.

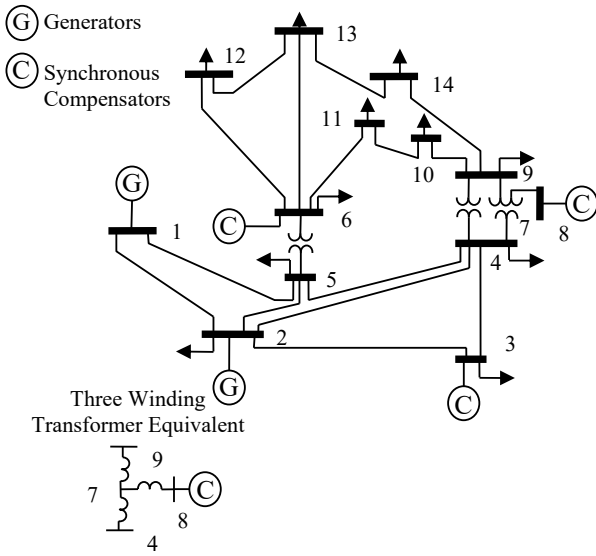


Fig. 7 IEEE 14-bus test system

The simulation was carried out with NEPLAN software, the aim of this work is to illustrate the role of the UPFC in improving the power flow, and reducing the power system losses and enhancing the voltage stability, so the steps being taken are:

1. Load flow solution with and without the UPFC.
2. Voltage stability analysis using modal analysis approach of the test system with and without the UPFC.

VI. SIMULATION RESULTS

A. Case 1

The load flow solution is applied on the IEEE 14-bus test system with the UPFC located between Bus 1 and Bus 5, and the results are compared to the test system without the UPFC, to see its effect on the power flow in the steady state conditions, the results are shown in Table II.

We can see from the results that the total active power losses get decreased from 13.59 MW to 13.43 MW and the total reactive power losses get decreased from 27.43 MVAR to 0.89 MVAR, by installing the UPFC in line 2 between Bus 1 and Bus 5.

From Table II, it can be observed that the active power flow in the lines changes by incorporating the UPFC, the lines 1, 3, 4, 5 were relieved by increasing the power transmitted in line 2, 6, 7, which represent an improvement of the load flow.

B. Case 2

The voltage stability analysis was performed on the 14-bus test system without the UPFC and with the UPFC located between Bus 1 and Bus 5; the results are shown in Figs. 8-10.

From Fig. 8 (a), we can see that the lowest system eigenvalues are above zero, confirming that the system is stable and the bifurcation point has not yet been reached. The minimum or critical eigenvalue is 2.079. This critical value is

increased as shown in Fig. 8 (b) from 2.079 to 2.688 by incorporating the UPFC in the test system, so the UPFC increases the stability margin.

TABLE II
LOAD FLOW RESULTS

	Without UPFC		With UPFC	
	P(MW)	Q(MVAR)	P(MW)	Q(MVAR)
line 1	157,14	-20,464	148,979	-18,538
line 2	75,457	5,584	83,458	12,284
line 3	73,477	3,537	71,661	3,717
line 4	55,931	1,795	53,332	-3,698
line 5	41,72	3,345	38,416	-5,712
line 6	23,46	-9,407	25,174	-3,828
line 7	60,116	-8,89	64,666	5,115
line 8	27,152	-5,922	27,359	-3,659
line 9	15,485	2,928	15,622	3,99
line 10	45,768	10,878	45,373	18,869
line 11	8,221	8,659	8,016	7,521
line 12	8,049	3,146	7,989	3,003
line 13	18,298	9,857	18,169	9,268
line 14	0	24	0	22,323
line 15	27,152	15,709	27,359	16,466
line 16	4,452	-0,681	4,628	0,393
line 17	8,686	0,465	8,853	1,154
line 18	4,603	6,611	4,415	5,511
line 19	1,869	1,379	1,81	1,24
line 20	6,406	4,935	6,23	4,226
Total Losses	13,597	27,434	13,438	0,895

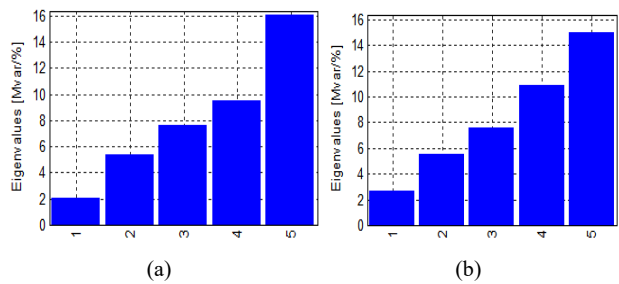


Fig. 8 Eigenvalues of the test system (a) without the UPFC and (b) with the UPFC

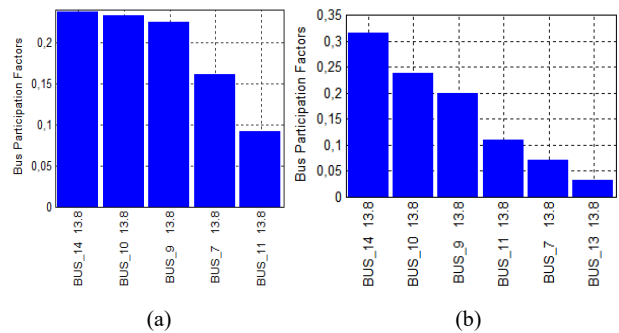


Fig. 9 Bus participation factor of the critical Eigenvalue of the test system (a) without the UPFC and (b) with the UPFC

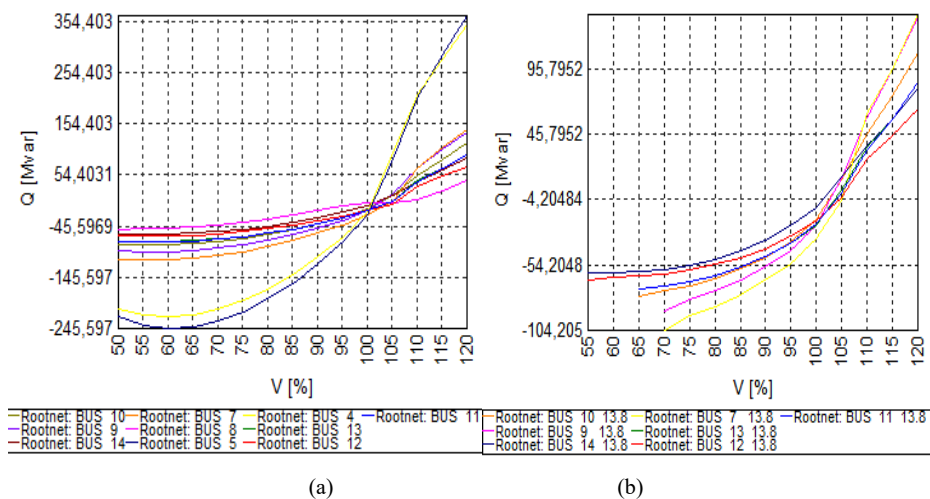


Fig. 10 Q-V curves of the test system (a) without the UPFC and (b) with the UPFC

Bus participation factors to the critical eigenvalue are generated to predict the critical buses in the system. The buses with highest participation factors were expected to be the most critical buses or buses closest to instability. These include bus 7, 9, 10, 11 and 14 as shown in Fig. 9. Moreover, the Q-V curve in Fig. 10 of the reactive stability margins shows that the UPFC offer the reactive power support needed to maintain stability. The curves clearly show that the reactive power margin of the power system without the UPFC is much bigger than the reactive power margin of the power system with the UPFC installed, this means that the power system with the UPFC requires less reactive power support to maintain stability.

VII. CONCLUSIONS

In this work, the effect of the UPFC on the losses and the voltage stability has been studied using load flow and voltage stability analysis of NEPLAN software modules, the load flow module, and the voltage stability module, which provides several approaches for static voltage stability analysis of power systems such as V-Q curves and Q-V eigenvalue analysis (modal analysis).

The results show that the UPFC can provide an adjusted distribution of the power flow among the transmission lines, minimize the system losses and enhance the voltage stability.

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