# Improvement of Photoluminescence Uniformity of Porous Silicon by using Stirring Anodization **Process**

Jia-Chuan Lin, Meng-Kai Hsu, Hsi-Ting Hou and Sin-Hong Liu

**Abstract**—The electrolyte stirring method of anodization etching process for manufacturing porous silicon (PS) is reported in this work. Two experimental setups of nature air stirring (PS-ASM) and electrolyte stirring (PS-ESM) are employed to clarify the influence of stirring mechanisms on electrochemical etching process. Compared to traditional fabrication without any stirring apparatus (PS-TM), a large plateau region of PS surface structure is obtained from samples with both stirring methods by the 3D-profiler measurement. Moreover, the light emission response is also improved by both proposed electrolyte stirring methods due to the cycling force in electrolyte could effectively enhance etch-carrier distribution while the electrochemical etching process is made. According to the analysis of statistical calculation of photoluminescence (PL) intensity, lower standard deviations are obtained from PS-samples with studied stirring methods, i.e. the uniformity of PL-intensity is effectively improved. The calculated deviations of PL-intensity are 93.2, 74.5 and 64, respectively, for PS-TM, PS-ASM and PS-ESM.

Keywords-Porous Silicon, Photoluminescence, Uniformity Carrier Stirring Method

#### I. INTRODUCTION

Porous silicon (PS) consisted of many nano pores and pillars has been demonstrative. pillars has been demonstrated to be a possible silicon-based material to yield efficient visible photoluminescence at room temperature [1]-[2]. Such optical property of light emission is attributed to the electron confinement in the nanocrystals that constitute the PS membrane [3]-[5]. Based on its unique porous nanostructure, PS material could simultaneously exhibit a high impendence, wide band-gap energy and high surface to volume ration (SVR) that are larger than conventional bulk Si. Therefore, many researches have widely carried out the PS technique development of electronic device design including the bio-sensor [6]-[7], hydrogen gas/photo detector [8]-[9], thermal sensor/isolator [10]-[12] and integrated circuit (IC) [13]-[14].

For PS manufacture, there are many techniques have been developed including electrochemical anodization [1],

Jia-Chuan Lin is with the Department of Electronics Engineering, St. John's Taipei 25135, Taiwan, R.O.C. jclin@mail.sju.edu.tw)Meng-Kai Hsu is with the Department of Electronic Engineering, St. John's University, Taipei 25135, Taiwan, R.O.C. (e-mail: mengkai@mail.sju.edu.tw).

Hsi-Ting Hou is with the Department of Electrical Engineering, TamKang University, Taipei 25137, Taiwan, R.O.C. (e-mail: jclin@mail.sju.edu.tw)

Sin-Hong Liu is with the Department of Chemical and Materials Engineering and Master Program of Nanomaterials, Chinese Culture University, Taipei 11114, Taiwan, R.O.C. (e-mail: sjujclin@gmail.com).

[15]-[16], spark erosion [17], stain etching [18]-[19], sol-gel [20] and vapor etching methods [21]-[22]. Especially, the electrochemical anodization etching is the most popular method for PS manufacture.

It is because of the low-cost and convenience of wet-etching process. In general, the illustrative equation of the overall process during PS electrochemical etching process can be expressed as [23]:

$$Si + 2HF + 2h^{+} \rightarrow SiF_{2} + 2H^{+}$$
 (1)

$$SiF_2 + 4HF \rightarrow H_2 + H_2SiF_6 \tag{2}$$

According to the chemical reaction equation, there are two major parameters to affect the etching rate of fabricated PS film; one is the hole  $(h^+)$  concentration of used Si-wafer, and another is the electrolyte concentration of HF-based soultion. According to the reaction mechanism of  $h^+$ , an improved manufacturing process of PS film with the application of Hall-effect was firstly proposed by Lin et al [16]. Based on its extra magnetic field, not only film uniformity of PS film but also a pattern-able profile is achieved in this report.

Thus, it should be found that the third interesting parameter of the etching carrier distribution surrounding the region between Si-surface and electrolyte is emerged. It is owing to the total current path of applied bias for electrochemical etching process is constituted by above two parameters of  $h^+$  and HF concentration. Therefore, we report the stirring method of anodization process for PS manufacture in this work. The experimental setup and procedures of our stirring method will be described in following section. In the section of experimental results, the statistic calculation is employed to analyze the influence of stirring process on PS manufacture. Finally, conclusions are made.

### II. EXPERIMENTS

In the present study, a vertical-design of experimental setup is employed to fabricate PS film (referred to the central-part of Fig. 1(a) or (b)). The Teflon materials are adopted to form the main-body of HF-based electrolyte container. Here, a mixture of HF:  $C_2H_5OH = 1:3$  is used as the etching solvent for all studied PS samples. A copper (Cu) disk is set as the anode, while the cathode is made of platinum (Pt). It should be noted that the bottom reaction area was about  $1.21\pi$  cm<sup>2</sup>. Here, the vertical arrangement of setup design could effectively reduce

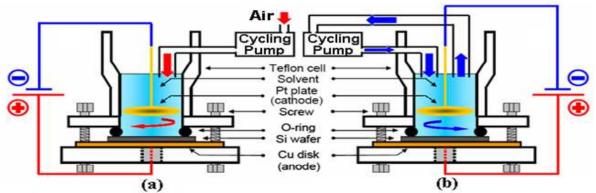


Fig. 1 Schematic diagram of experimental setup with (a) nature air stirring (ASM) and (b) electrolyte stirring (ESM) for preparing PS film

the hydrogen bubbles accumulation on PS surface while the electrochemical etching is made, and therefore improve the uniformity of fabricated PS samples. On the other hand, the (100)-oriented n-type Si-wafer with a resistivity of 1-10  $\Omega$ cm is used for preparing all studied PS samples. Before the electrochemical anodization etching process, an aluminum (Al) film is deposited on the backside of Si-wafer as a contact electrode and annealed as ohmic-contact to allow a homogeneous anodization current flow. The anodization etching current is supplied by an external constant current source which is kept at 60 mA.

To investigate the stirring influence on PS manufacture, two apparatus designs of nature air stirring (ASM) and electrolyte stirring (ESM) are added into the traditional setup as shown in fig. 1 (a) and (b), respectively. The employee of cycling pump is the key part used to create an extra force in HF-based electrolyte, and therefore induce a stirring distribution of hole etching carrier.

#### III. RESULTS AND DISCUSSION

To optimize the electrochemical etching process, the PS film made by traditional method (TM) with a fixed anodiztion current (I = 60 mA) and various etching times (from 10 min to 30 min with 5 min/step) is firstly performed. Fig. 2 shows the measured PL-spectra of fabricated PS films. Obviously, the obtained peak value of PL-intensity is depended on the electrochemical etching time in this work. The corresponding wavelength of intensity peak for all PS-TM samples with different etching time are around 600 nm.

Such optical properties of orange-red light emission are believed to that the quantum confinement effect in the nanostructure of fabricated PS film. The related effect for illustrating the luminescence on PS structure has bee reported by [4], [24]. On the other hand, the increase in peak value is with the increase in etching time. Especially, the maximum intensity is obtained from PS-TM sample with etching time of 30 min. It is attributed to the deep size effect of fabricated PS film [24].

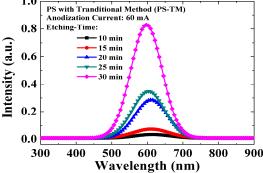


Fig. 2 The measured PL-spectra of PS samples fabricated by traditional method with various etching times (10 min to 30 min, 5 min/step) at fixed anodization current of 60 mA

Based on this optimum etching time condition of 30 min with fixed etching current of 60 mA, the stirring methods of ASM and ESM are following performed to comprehend the related influences. Fig. 3 shows the measured PL-spectra for PS films fabricated with methods of TM, ASM and ESM. Here, a blue-shift and red-shift of light emission properties are observed from PS-ASM and PS-ESM, respectively. Such behavior is believed to the etching reaction is affect by the stirring forces of both studied methods. For PS-ESM, the force is mainly created by cycling electrolyte. Then, the etching carrier distribution between the electrolyte and Si-wafer is enhanced without any concentration loss.

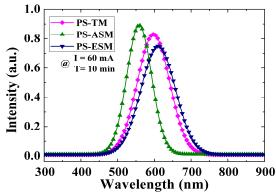


Fig. 3 The measured PL-spectra of PS-TM, PS-ASM and PS-ESM (excitation wavelength of 325 n He-Cd laser). The etching condition are fixed at  $T=10 \ \text{min}$  and  $I=60 \ \text{mA}$ 

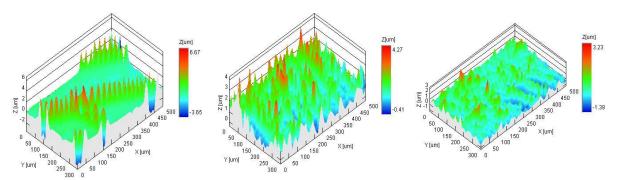


Fig. 4 The measured 3D profiles of studied samples of PS-TM, PS-ASM and PS-ESM (T = 10 min and I = 60 mA)

Therefore, a more complete anodization etching property is obtained from sample with electrolyte stirring method.

On the contrary, the manufacture of PS-ASM would bring the some extra nature air into the electrolyte and thus induce a loss in electrolyte concentration that compared with PS-ESM. However, the carrier distribution of anodization current is still stronger than PS-TM. Based on such mechanism, a few differences in deep-size of PS nanostructure are occurred on studied samples with different etching method. The corresponding wavelength of PL-intensity peak are 597 nm, 560 nm and 609 nm for PS-TM, PS-ASM and PS-ESM, respectively. To further clarify the influence of PS sample with/without stirring method, the statistic calculations of obtained PL-spectra are utilized to analyze uniformity improvement. The formula of standard deviation is expressed as below:

$$S = \sqrt{\frac{\sum_{i=1}^{n} (Xi - \overline{X})^{2}}{n}}$$
 (3)

Where S is the roughness of PL intensity, Xi is the wavelength of intensity peak,  $\overline{X}$  is the mean value of PL intensity and n is the sampling number. In general, the smaller S means the uniformity of PL intensity of studied PS sample is better. The calculated S values are 93.2, 74.5 and 64 for PS-TM, PS-ASM and PS-ESM, respectively. Calculated results indicated that the obtained S of PS samples with both stirring method are lower than traditional method. Therefore, the uniformity of PL property could effectively improved by stirring anodization method. On the other hand, the 3D-profiler is utilized to observe the surface roughness of PS sample with/without stirring method. Obviously, a flattest PS surface is obtained from PS-ESM. It's attributed that there is no any unnecessary parameter such as a loss of electrolyte concentration appeared during the etching reaction.

## IV. CONCLUSION

The comparative study of stirring anodization process for PS fabrication by using the nature air stirring and electrolyte stirring were present in this work. It is found that the proposed stirring methods could effectively create an extra cycling force in electrolyte, and therefore enhance the hole  $(h^+)$  carrier

distribution of electrochemical etching process. The corresponding wavelength of PL-intensity peak are 597 nm, 560 nm and 609 nm for PS-TM, PS-ASM and PS-ESM, respectively. Experimental results demonstrate that the uniformity improvements of structure profile and PL-intensity are obtained from studied PS sample with process of ASM or ESM. The calculated deviations of PL-intensity are 93.2, 74.5 and 64, respectively, for PS-TM, PS-ASM and PS-ESM. For the further applications of PS-based material such as electronic device, optical device and biosensor, the proposed stirring methods could provide an interesting approach to yield a high quality PS film.

#### ACKNOWLEDGMENT

This work is financially supported by the National Science Council of Republic of China under contact NSC 99-2632-E129-001-MY3.

#### REFERENCES

- L. T. Canham, "Silicon quantum wire array fabrication by electrochemical and chemical dissolution of wafers," *Appl. Phys. Lett.*, vol. 57, pp. 1046–1048, July 1990.
- [2] P. McCord, S. L. Yau and A. J. Bard, "Chemiluminescence of anodized and etched silicon: evidence for a luminescent siloxene-like layer on porous silicon," *Science*, vol. 257, pp. 68–69, Jul. 1992.
- [3] P. Fauchet, "Photoluminescence and electroluminescence from porous silicon," *J. Lumin.*, vol. 257, pp. 294–309, Oct. 1996.
  [4] A. G. Cullis, L. T. Canham and P. D. J. Calcott, "The structural and
- [4] A. G. Cullis, L. T. Canham and P. D. J. Calcott, "The structural and luminescence properties of porous silicon," *J. Appl. Phys.*, vol. 82, pp. 909–965, Apr. 1997.
- [5] O. Bisi, S. Ossicine and L. Pavesi, "Porous silicon: a quantum sponge structure for silicon based optoelectronics," *Surf. Sci. Rep.*, vol. 38, pp. 1–126, Apr. 2000.
- [6] F. P. Mathew and E. C. Alocilja, "Porous silicon-based biosensor for pathogen detection," *Biosens. Bioelectron.*, vol. 20, pp. 1656-1661, Feb. 2005.
- [7] M. J. Schoning, A. Kurowski, M.Thust, P. Kordos, J. W. Schultze and H. Luth, "Capacitive microsensors for biochemical sensing based on porous silicon technology," *Sensor. Actuat. B Chem.*, vol. 64, pp. 59-64, Jun. 2000
- [8] V. Polishchuk, E. Souteyrand, J. R. Martin, V. I. Strikha and V. A. Skrysheveky, "A study of hydrogen detection with palladium modified porous silicon," *Anal. Chim. Acta.*, vol. 375, pp. 205-210, Nov. 1998.
- [9] K. Luongo, A. Sine and S. Bhansali, "Development of a highly sensitive porous Si-based hydrogen sensor using Pd nano-structures," *Sensor. Actuat. B Chem.*, vol. 111-112, pp. 125-129, Nov. 2005.
- [10] C. Tsamis, A. G. Nassiopoulou and A. Tserepi, "Thermal properties of suspended porous silicon micro-hotplates for sensor applications," *Sensor. Actuat. B Chem.*, vol. 95, pp. 78-82, Oct. 2003.

- [11] P. Y. Y. Kan and T. G. Finstad, "Oxidation of macroporous silicon for thick thermal insulation," *Mat. Sci. Eng. B-Adv.*, vol. 118, pp. 289-292, Apr. 2005.
- [12] M. Bjorkqvist, J. Salonen, J. Paski, E. Laine, "Characterization of thermally carbonized porous silicon humidity sensor," *Sensor. Actuat. A Phys.*, vol. 112, pp. 244-247, May 2004.
- [13] H. Contopanagos and A. G. Nassiopoulou, "Design and simulation of integrated inductors on porous silicon in CMOS-compatible processes," *Solid State Electron.*, vol. 50, pp. 1283-1290, Aug. 2006.
- [14] C. Li, H. Liao, L. Yang and R. Huang, "High-performance integrated inductor and effective crosstalk isolation using post-CMOS selective grown porous silicon (SGPS) technique for RFIC applications," *Solid State Electron.*, vol. 51, pp. 989-994, Jun. 2007.
- [15] L. T. Canham, W. Y. Leong, T. I. Cox and L. Taylor, "Efficient visible electroluminescence from highly porous silicon under cathodic bias," *Appl. Phys. Lett.*, vol. 61, pp. 2563-2565, Sep. 1992.
- [16] J. C. Lin, P. W. Lee and W. C. Tsai, "Manufacturing method for n-type porous silicon based on Hall effect without illumination," *Appl. Phys. Lett.*, vol. 89, pp. 12119-1-3, Sep. 2006.
- [17] A. Richter, P. Steiner, F. Kozlowski and W. Lang, "Current-induced light emission from a porous silicon device," IEEE Electron Device Lett., vol. 12, pp. 691-692, Dec. 1991.
- [18] J. Sarahy, S. Shih, K. Jung, C. Tsai, K. H. Li, D. L. Kwong, J. C. Campbell, S. L. Yau and A. J. Bard, "Demonstration of photoluminescence in nonanodized silicon," Appl. Phys. Lett., vol. 60, pp. 1532-1535. Jan. 1992.
- [19] A. Ksendzov, R. W. Fathauer, T. George, W. T. Pike, R. P. Vasquez, and A. P. Taylor, "Visible photoluminescence of porous Si1–xGex obtained by stain etching," *Appl. Phys. Lett.*, vol. 63, pp. 200-202, Apr. 1993.
- [20] N. V. Gaponenko, "Sol-gel derived films in meso-porous matrices: porous silicon, anodic alumina and artificial opals," *Synth. Met.*, vol. 124, pp. 125-130, Oct. 2001.
- [21] S. Kalem and O. Yavuzcetin, "Possibility of fabricating light-emitting porous silicon from gas phase etchants," Opt. Exp., vol. 6, pp. 7-11, Jan. 2000
- [22] M. Saadoun, N. Mliki, H. Kaabi, K. Daoudi, B. Bessais, H. Ezzaouia and R. Bennaceur, "Vapour-etching-based porous silicon: a new approach," *Thin Solid Films*, vol. 405, pp. 29-34, Feb. 2002.
- [23] T. Unagami, "Formation Mechanism of Porous Silicon Layer by Anodization in HF Solution," *J. Electrochem. Soc.*, vol.127, pp. 476-486, Feb. 1980.
- [24] J. C. Lin, W. C. Tsai and W. L. Chen, "Light emission and negative differential conductance of n-type nanoporous silicon with buried p-layer assistance," Appl. Phys. Lett., vol. 90, pp. 09117 (1-3), Mar. 2007.



**Jia-Chuan Lin** was born in Taipei, Taiwan, R.O.C., on January 31, 1967. He received the B.S., M.S. and Ph.D. degrees in electrical engineering from the National Cheng-Kung University, Tainan, Taiwan, in 1990, 1992 and 1996, respectively.

From 1997 to 2001 and 2001 to 2004, he was in the Department of Electronics Engineering, St. John's College, Taipei, and the Department of Electrical Engineering,

Chinese Culture University, where he was an Associate Professor, respectively. He has been with the Department of Electronics Engineering, St. John's University, Taipei, Taiwan, since 2004, where he was promoted as a Full Professor in 2007. He is also the Vice President of St. John's University since 2007. His current research interests include Si-based material, porous silicon material and III-V compound semiconductor/devices including photo-detector, biosensor, solar-cell, LED, HFET and HBT.



Meng-Kai Hsu was born in Hua-Lien, Taiwan, R.O.C., on September 7, 1978. He received the B.S. degree in electrical engineering from Private Chinese Culture University, Taipei, Taiwan, in 2002. Then, he received the M.S. and Ph.D. degrees in electrical engineering from the National Taiwan Ocean University, Keelung, Taiwan, in 2004 and 2008, respectively.

He is now served as post-doctor researcher in department of electronics engineering in St. John's University since 2009, and he is also an Adjunct Associate Professor in department of chemical and materials engineering, Chinese Culture University. His current research interests are in III-V

compound and Si-based semiconductor materials and devices in major. They include HEMT, HBT, porous silicon engineering, solar-cell and LED.



**Hsi-Ting Hou** was born in Taipei, Taiwan, R.O.C., on August 15, 1990. He received the B.S. and M.S. degrees in electronics engineering from St. John's University, Taipei, Taiwan in 2004 and 2006, respectively. He is currently working toward to the Ph.D. degree in the electrical engineering from TamKang University, Taipei.

His research has focused on the field of Si-based semiconductor materials/devices and the organic materials,

especially at porous silicon optical devices and sensors.



**Sin-Hong Liu** was born in Taipei, Taiwan, R.O.C. on January 19, 1987. He received the B.S. and M.S. degrees in chemical and materials engineering from Private Chinese Culture University Taipei, Taiwan in 2009 and 2011, respectively.

His research has focused on the fields of electrochemical engineering and the Si-based semiconductor materials, especially at porous silicon manufacture and device process.